

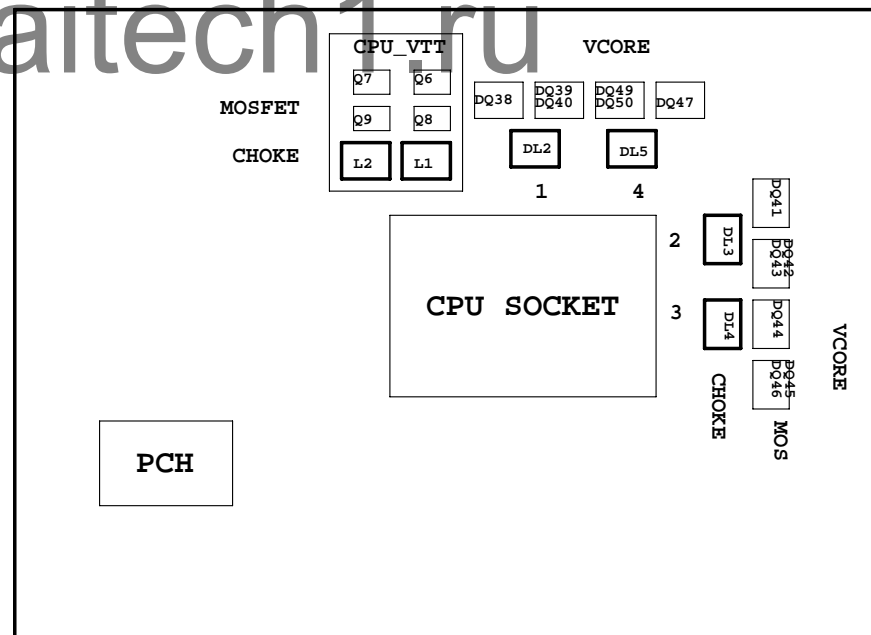
Model Name: GA-P61-USB3-B3 1.01

SHEET TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1155-A
05	CPU_LGA1155-B
06	CPU_LGA1155-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE,NVRAM
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS*4 SLOT
16	PCI EXPRESS*1 SLOTS X2
17	PCI SLOT 1&2&3
18	I/O ITE8728
19	COM, -PROHOT, ESATA CONNECT
20	Dual BIOS
21	ALC892
22	REAR AUDIO JACK
23	VCORE PWM_ISL6364CRZ-1
24	VCORE PWM_ISL6364CRZ-2
25	DISCRETE POWER
26	DDR_15V & VCC1_05_PCH PWM_ISL6545CBZ
27	CPU_VTT PWM_ISL6322G

SHEET TITLE

28	VCCSA POWER
29	F_PANEL , F_USB
30	ATX POWER, CLOCK GEN
31	HWM,KB/MS , FAN CTRL
32	REALTEK RTL8111E
33	NEC USB3.0
34	TABLE LIST
35	
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## GA-P61-USB3-B3

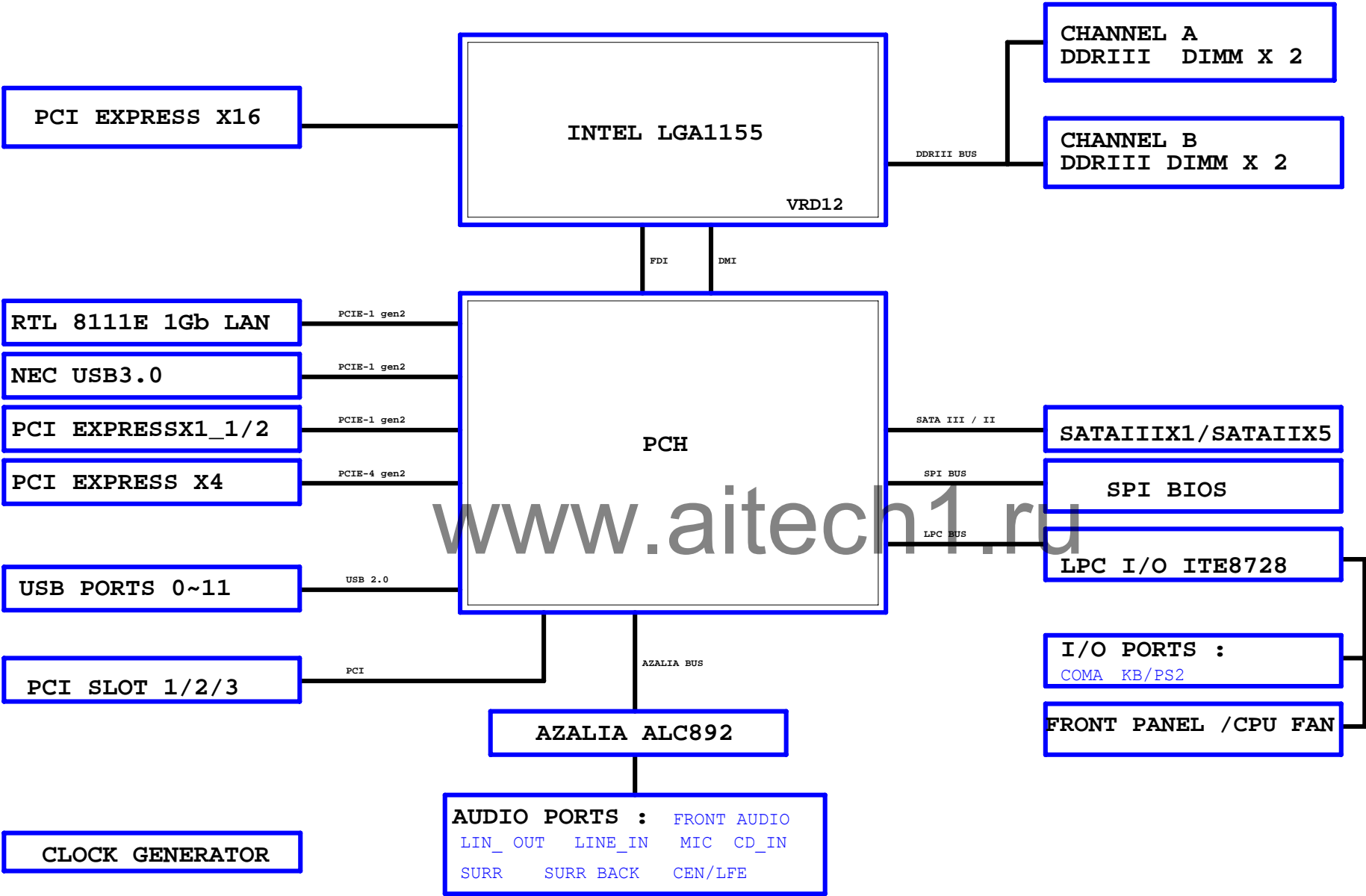
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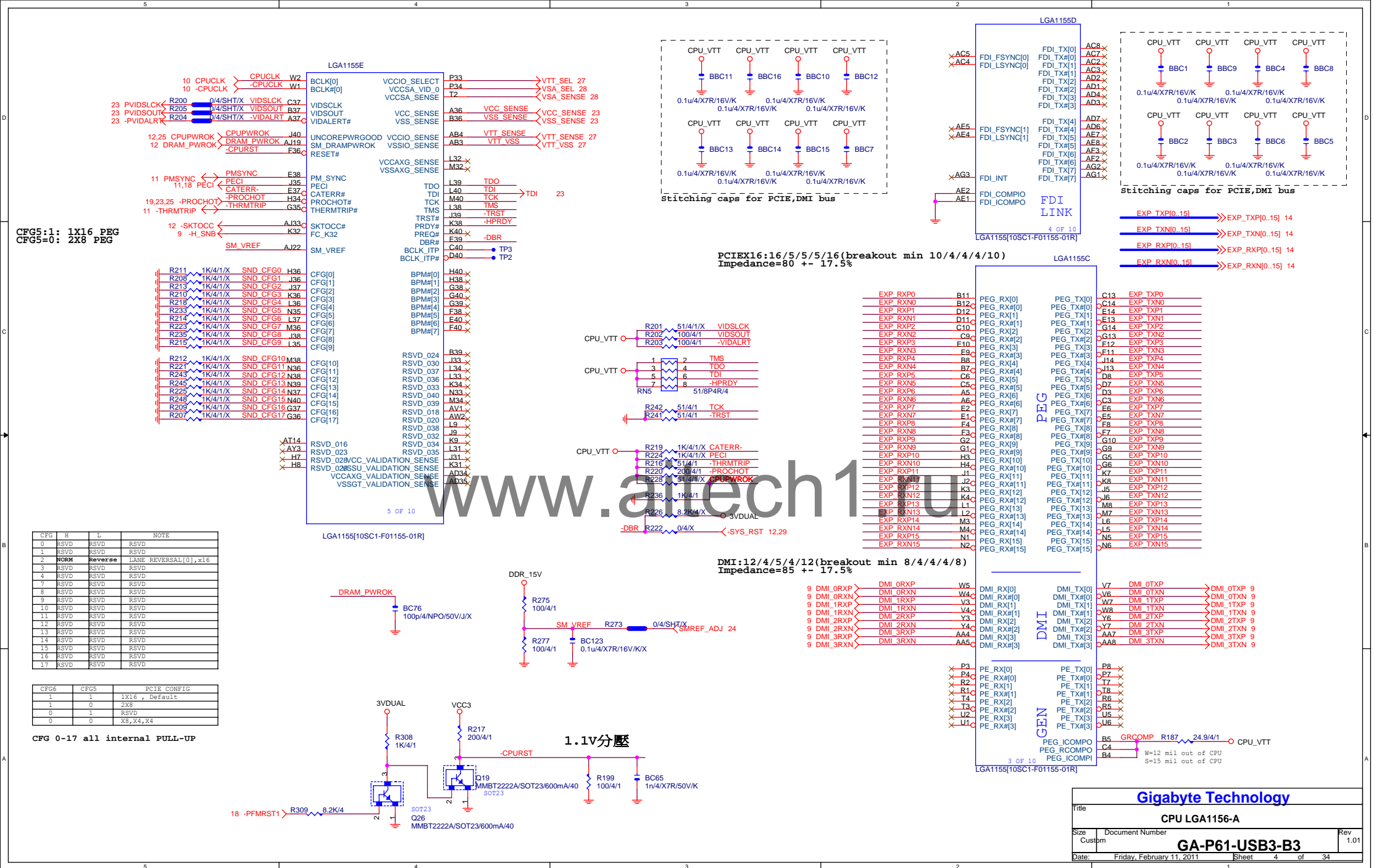
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## Circuit or PCB layout change

[illegible]

BLOCK DIAGRAM





LGA1155A

MAAA0	AV27	SA_MA[0]	SA_DQS[0]	AK3	DQSA0
MAAA1	AY24	SA_MA[1]	SA_DQS[0]	AK2	-DQSA0
MAAA2	AW24	SA_MA[2]			
MAAA3	AW23	SA_MA[3]			
MAAA4	AV23	SA_MA[4]	SA_DQ[0]	AJ3	MDA0
MAAA5	AT24	SA_MA[5]	SA_DQ[1]	AJ4	MDA1
MAAA6	AT23	SA_MA[6]	SA_DQ[2]	AL3	MDA2
MAAA7	AU22	SA_MA[7]	SA_DQ[3]	AL4	MDA3
MAAA8	AV22	SA_MA[8]	AJ2	MDA4	
MAAA9	AT22	SA_MA[9]	AJ1	MDA5	
MAAA10	AV28	SA_MA[10]	SA_DQ[6]	AL2	MDA6
MAAA11	AU21	SA_MA[11]	SA_DQ[7]	AL1	MDA7
MAAA12	AU21	SA_MA[12]			
MAAA13	AW32	SA_MA[13]	SA_DQS[1]	AP3	DQSA1
MAAA14	AU20	SA_MA[14]	SA_DQS[1]	AP2	-DQSA1
MAAA15	AT20	SA_MA[15]			
7 -SWEA	AW29	SA_WE#	SA_DQ[8]	AN1	MDA8
7 -SCASA	AV30	SA_CAS#	SA_DQ[9]	AN4	MDA9
7 -SRASA	AU28	SA_RAS#	SA_DQ[10]	AR3	MDA10
			SA_DQ[11]	AR4	MDA12
7 SBAA0	SBAA0	SA_BS[0]	SA_DQ[12]	AN2	MDA11
7 SBAA1	SBAA1	SA_BS[1]	SA_DQ[13]	AN3	MDA13
7 SBAA2	SBAA2	SA_BS[2]	SA_DQ[14]	AR2	MDA14
			SA_DQ[15]	AR1	MDA15
7 -CSA0	CSA0	SA_CS#	SA_DQS[2]	AW4	DQSA2
7 -CSA1	CSA1	SA_CS#	SA_DQS[2]	AW4	-DQSA2
			SA_DQS[2]	AW4	-DQSA2
7 CKEA0	CKEA0	SA_CKE[0]	SA_DQ[16]	AV2	MDA16
7 CKEA1	CKEA1	SA_CKE[1]	SA_DQ[17]	AW3	MDA17
			SA_DQ[18]	AV5	MDA18
			SA_DQ[19]	AW5	MDA20
			SA_DQ[20]	AU2	MDA21
			SA_DQ[21]	AU3	MDA22
			SA_DQ[22]	AY5	MDA23
			SA_DQ[23]	AK26	
			SA_DQS[3]	AV8	DQSA3
			SA_DQS[3]	AW8	-DQSA3
7 DCLKA0	DCLKA0	SA_CLK[0]	SA_DQ[24]	AY7	MDA24
7 -DCLKA0	-DCLKA0	SA_CLK[0]	SA_DQ[25]	AJ7	MDA25
7 DCLKA1	DCLKA1	SA_CLK[1]	SA_DQ[26]	AV9	MDA26
7 -DCLKA1	-DCLKA1	SA_CLK[1]	SA_DQ[27]	AJ9	MDA27
			SA_DQ[28]	AV7	MDA28
			SA_DQ[29]	AW7	MDA29
			SA_DQ[30]	AW9	MDA30
			SA_DQ[31]	AY9	MDA31
			SA_DQS[4]	AV37	DQSA4
			SA_DQS[4]	AV36	-DQSA4
			SA_DQ[32]	AJ35	MDA32
			SA_DQ[33]	AW37	MDA33
			SA_DQ[34]	AJ38	MDA34
			SA_DQ[35]	AJ36	MDA35
			SA_DQ[36]	AW35	MDA36
			SA_DQ[37]	AY36	MDA37
			SA_DQ[38]	AJ38	MDA38
			SA_DQ[39]	AJ37	MDA39
			SA_DQS[5]	AP38	DQSA5
			SA_DQS[5]	AP39	-DQSA5
			SA_DQ[40]	AR40	MDA40
			SA_DQ[41]	AR37	MDA41
			SA_DQ[42]	AN38	MDA42
			SA_DQ[43]	AN37	MDA43
			SA_DQ[44]	AR39	MDA44
			SA_DQ[45]	AR38	MDA45
			SA_DQ[46]	AN38	MDA46
			SA_DQ[47]	AN40	MDA47
			SA_DQS[6]	AK38	DQSA6
			SA_DQS[6]	AK39	-DQSA6
			SA_DQ[48]	AL40	MDA48
			SA_DQ[49]	AL37	MDA49
			SA_DQ[50]	AJ38	MDA50
			SA_DQ[51]	AJ37	MDA51
			SA_DQ[52]	AL39	MDA52
			SA_DQ[53]	AL38	MDA53
			SA_DQ[54]	AJ39	MDA54
			SA_DQ[55]	AJ40	MDA55
			SA_DQS[7]	AF38	DQSA7
			SA_DQS[7]	AF39	-DQSA7
			SA_DQ[56]	AG40	MDA56
			SA_DQ[57]	AG37	MDA57
			SA_DQ[58]	AE38	MDA58
			SA_DQ[59]	AE37	MDA59
			SA_DQ[60]	AG39	MDA60
			SA_DQ[61]	AG38	MDA61
			SA_DQ[62]	AE39	MDA62
			SA_DQ[63]	AE40	MDA63

DDR\_0

1 OF 10

LGA1155[10SC1-F01155-01R]

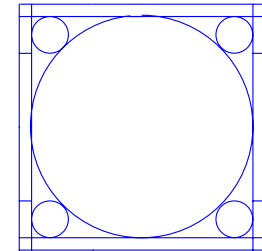
LGA1155B

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MAAB1	AM20	SB_MA[1]	SB_DQS[0]	AH6	-DQSB0
MAAB2	AM19	SB_MA[2]			
MAAB3	AK18	SB_MA[3]			
MAAB4	AP19	SB_MA[4]	SB_DQ[0]	AG7	MDB0
MAAB5	AP18	SB_MA[5]	SB_DQ[1]	AG8	MDB1
MAAB6	AM18	SB_MA[6]	SB_DQ[2]	AJ9	MDB2
MAAB7	AL18	SB_MA[7]	SB_DQ[3]	AJ8	MDB3
MAAB8	AN18	SB_MA[8]	SB_DQ[4]	AG5	MDB4
MAAB9	AY17	SB_MA[9]	SB_DQ[5]	AG6	MDB5
MAAB10	AN23	SB_MA[10]	SB_DQ[6]	AJ6	MDB6
MAAB11	AU17	SB_MA[11]	SB_DQ[7]	AJ7	MDB7
MAAB12	AT18	SB_MA[12]			
MAAB13	AR26	SB_MA[13]	SB_DQS[1]	AM8	DQSB1
MAAB14	AY16	SB_MA[14]	SB_DQS[1]	AL8	-DQSB1
MAAB15	AV16	SB_MA[15]			
8 -SWEB	SWEB	SB_WE#	SB_DQ[8]	AL7	MDB8
8 -SCASB	SCASB	SB_CAS#	SB_DQ[9]	AM7	MDB9
8 -SRASB	SRASB	SB_RAS#	SB_DQ[10]	AM10	MDB10
			SB_DQ[11]	AL10	MDB11
8 SBAB0	SBAB0	SB_BS[0]	SB_DQ[12]	AL6	MDB12
8 SBAB1	SBAB1	SB_BS[1]	SB_DQ[13]	AL9	MDB13
8 SBAB2	SBAB2	SB_BS[2]	SB_DQ[14]	AM9	MDB15
			SB_DQ[15]		
8 -CSB0	CSB0	SB_CS#	SB_DQS[2]	AR8	DQSB2
8 -CSB1	CSB1	SB_CS#	SB_DQS[2]	AP8	-DQSB2
			SB_DQS[2]		
8 CKEB0	CKEB0	SB_CKE[0]	SB_DQ[16]	AP7	MDB16
8 CKEB1	CKEB1	SB_CKE[1]	SB_DQ[17]	AR7	MDB17
			SB_DQ[18]	AP10	MDB18
			SB_DQ[19]	AR10	MDB19
			SB_DQ[20]	AP6	MDB20
			SB_DQ[21]	AP9	MDB21
			SB_DQ[22]	AR9	MDB22
			SB_DQ[23]	AR9	MDB23
			SB_DQS[3]	AN13	DQSB3
			SB_DQS[3]	AN12	-DQSB3
8 DCLKB0	DCLKB0	SB_CLK[0]	SB_DQ[24]	AM12	MDB24
8 -DCLKB0	-DCLKB0	SB_CLK[0]	SB_DQ[25]	AM13	MDB25
8 DCLKB1	DCLKB1	SB_CLK[1]	SB_DQ[26]	AR13	MDB26
8 -DCLKB1	-DCLKB1	SB_CLK[1]	SB_DQ[27]	AP13	MDB27
			SB_DQ[28]	AL12	MDB28
			SB_DQ[29]	AL13	MDB29
			SB_DQ[30]	AR12	MDB30
			SB_DQ[31]	AP12	MDB31
			SB_DQS[4]	AN28	DQSB4
			SB_DQS[4]	AN28	-DQSB4
			SB_DQ[32]	AR28	MDB32
			SB_DQ[33]	AR23	MDB33
			SB_DQ[34]	AL28	MDB34
			SB_DQ[35]	AL29	MDB35
			SB_DQ[36]	AP28	MDB36
			SB_DQ[37]	AP29	MDB37
			SB_DQ[38]	AM28	MDB38
			SB_DQ[39]	AM29	MDB39
			SB_DQS[5]	AP33	DQSB5
			SB_DQS[5]	AR33	-DQSB5
			SB_ECC_CB[0]		
			SB_ECC_CB[1]		
			SB_ECC_CB[2]		
			SB_DQ[40]	AP32	MDB40
			SB_DQ[41]	AP31	MDB41
			SB_DQ[42]	AP35	MDB42
			SB_DQ[43]	AP34	MDB43
			SB_DQ[44]	AR31	MDB44
			SB_DQ[45]	AR32	MDB45
			SB_DQ[46]	AR34	MDB47
			SB_DQ[47]		
			SB_DQS[6]	AL33	DQSB6
			SB_DQS[6]	AM33	-DQSB6
			SB_DQ[48]	AM32	MDB48
			SB_DQ[49]	AM31	MDB49
			SB_DQ[50]	AL35	MDB50
			SB_DQ[51]	AL32	MDB51
			SB_DQ[52]	AM34	MDB52
			SB_DQ[53]	AL31	MDB53
			SB_DQ[54]	AM35	MDB54
			SB_DQ[55]	AL34	MDB55
			SB_DQS[7]	AG35	DQSB7
			SB_DQS[7]	AG34	-DQSB7
			SB_DQ[56]	AH35	MDB56
			SB_DQ[57]	AH34	MDB57
			SB_DQ[58]	AE34	MDB58
			SB_DQ[59]	AE35	MDB59
			SB_DQ[60]	AJ35	MDB60
			SB_DQ[61]	AJ34	MDB61
			SB_DQ[62]	AE33	MDB62
			SB_DQ[63]	AE33	MDB63

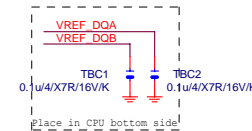
DDR\_1

2 OF 10

LGA1155[10SC1-F01155-01R]

LGA1155  
ILM\_BP/1156/CSP

Need check the new CPU ME

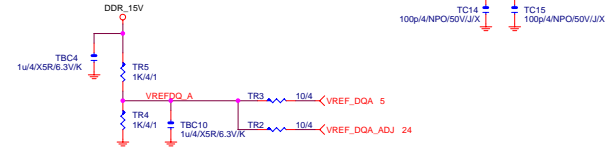
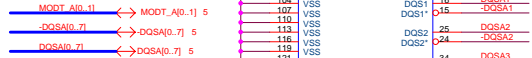


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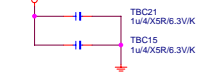
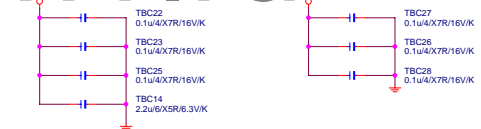
CPU LGA1156-B

Title	Document Number	Rev
Size	Custom	1.01
Date:	Friday, February 11, 2011	Sheet 5 of 34

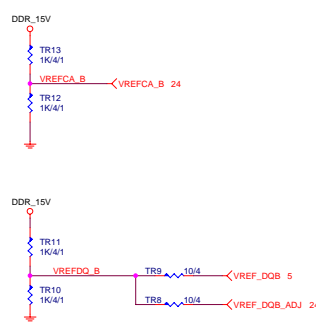
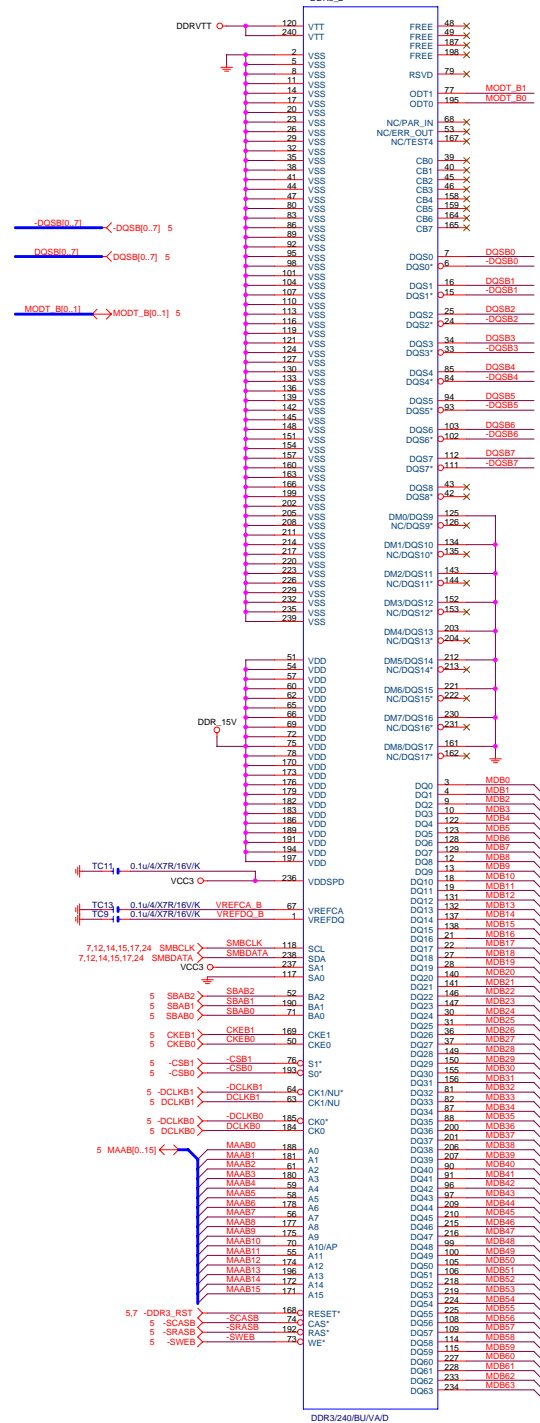




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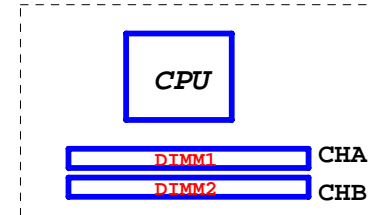
DDR3 1066,1333,1600MHZ BANDWIDTH

DDR3 1066MHZ  
DDR3 clock=533MHZ  
DDR3 single channel bandwidth=533x2x8Byte=8.5GB/s  
DDR3 dual channel bandwidth=533x2x2x8Byte=17GB/s

DDR3 1333MHZ  
DDR3 clock=667MHZ  
DDR3 single channel bandwidth=10.6GB/s  
DDR3 dual channel bandwidth=21GB/s

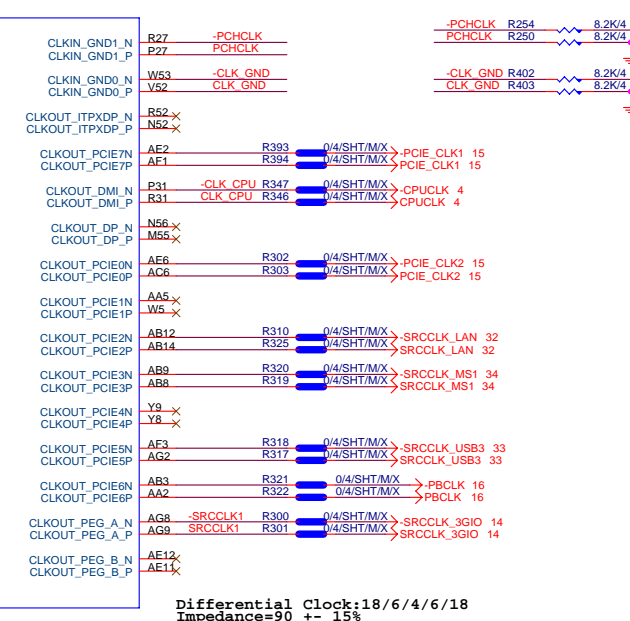
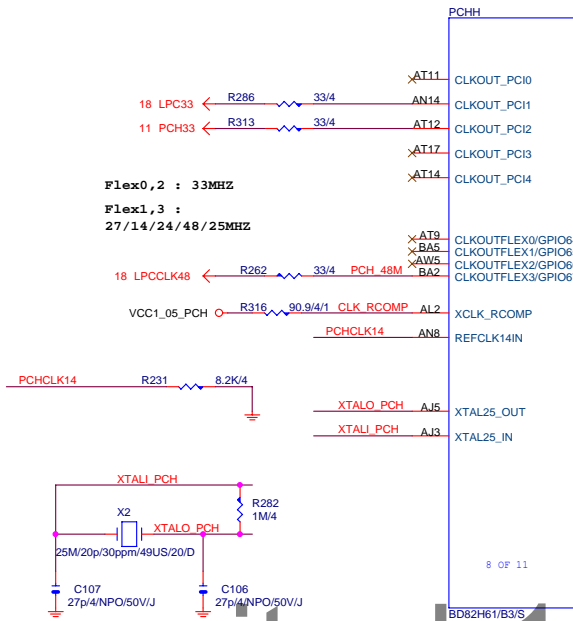
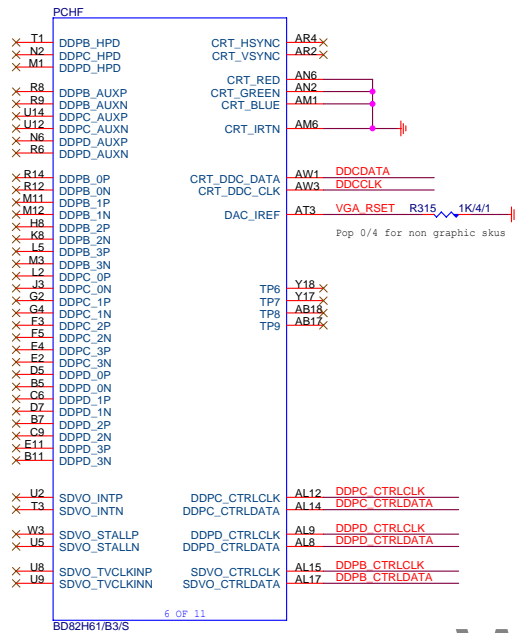
DDR3 1600MHZ  
DDR3 clock=800MHZ  
DDR3 single channel bandwidth=12.8GB/s  
DDR3 dual channel bandwidth=25.6GB/s

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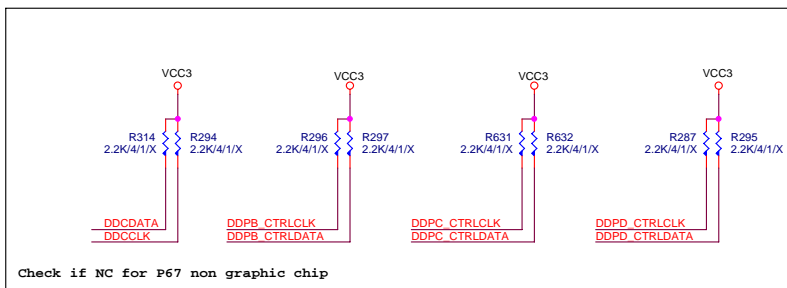


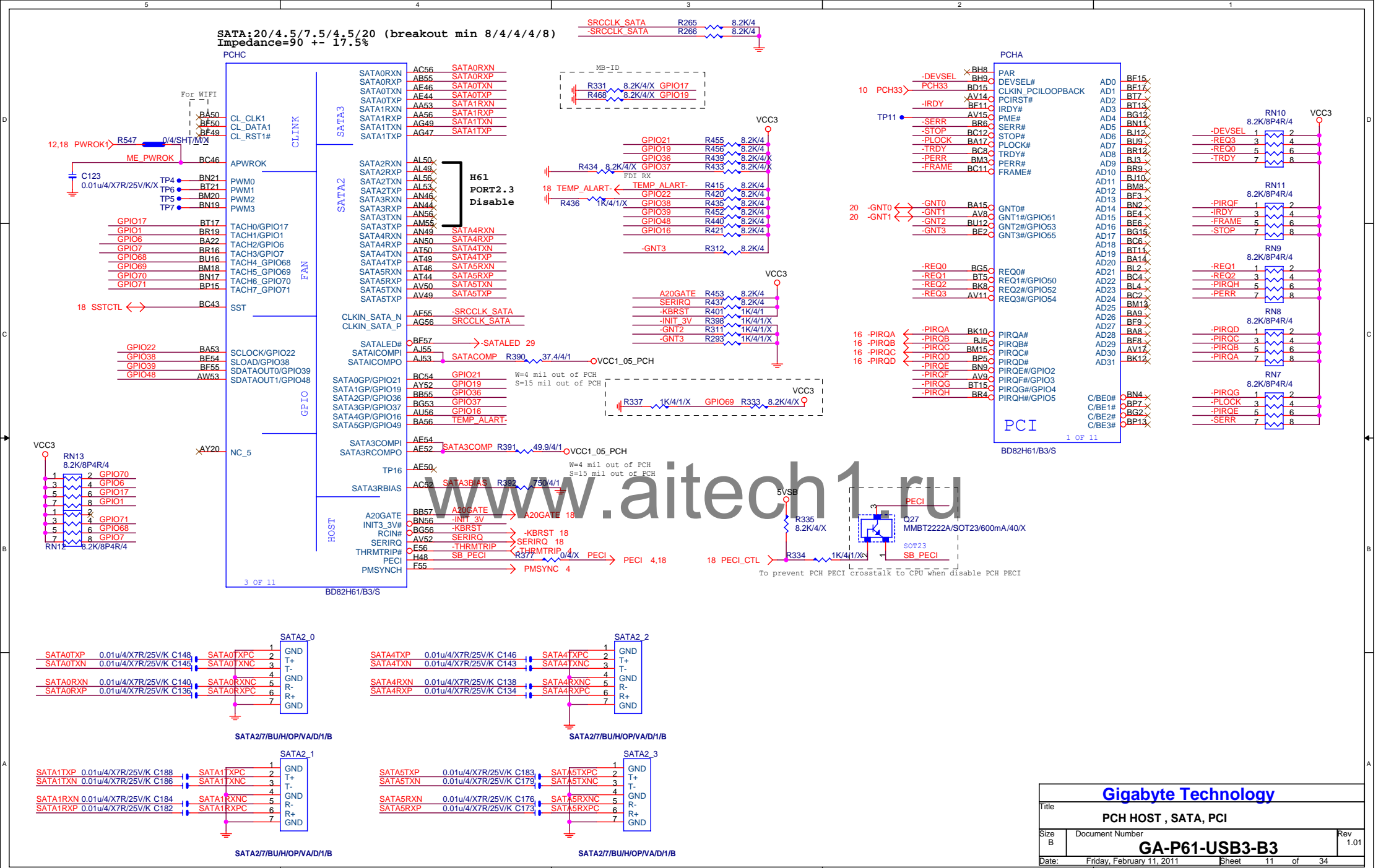




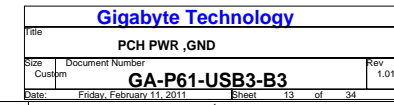


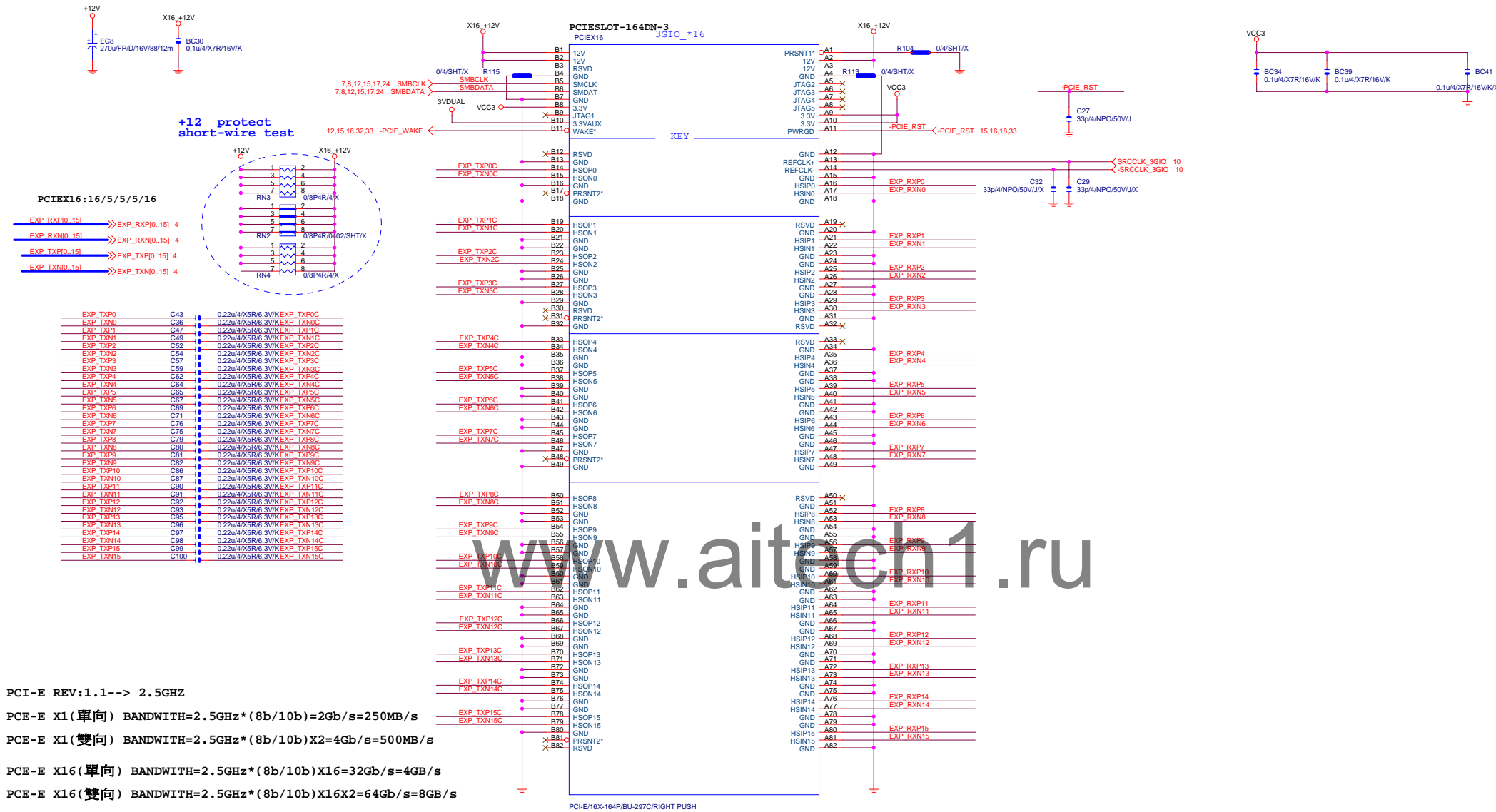
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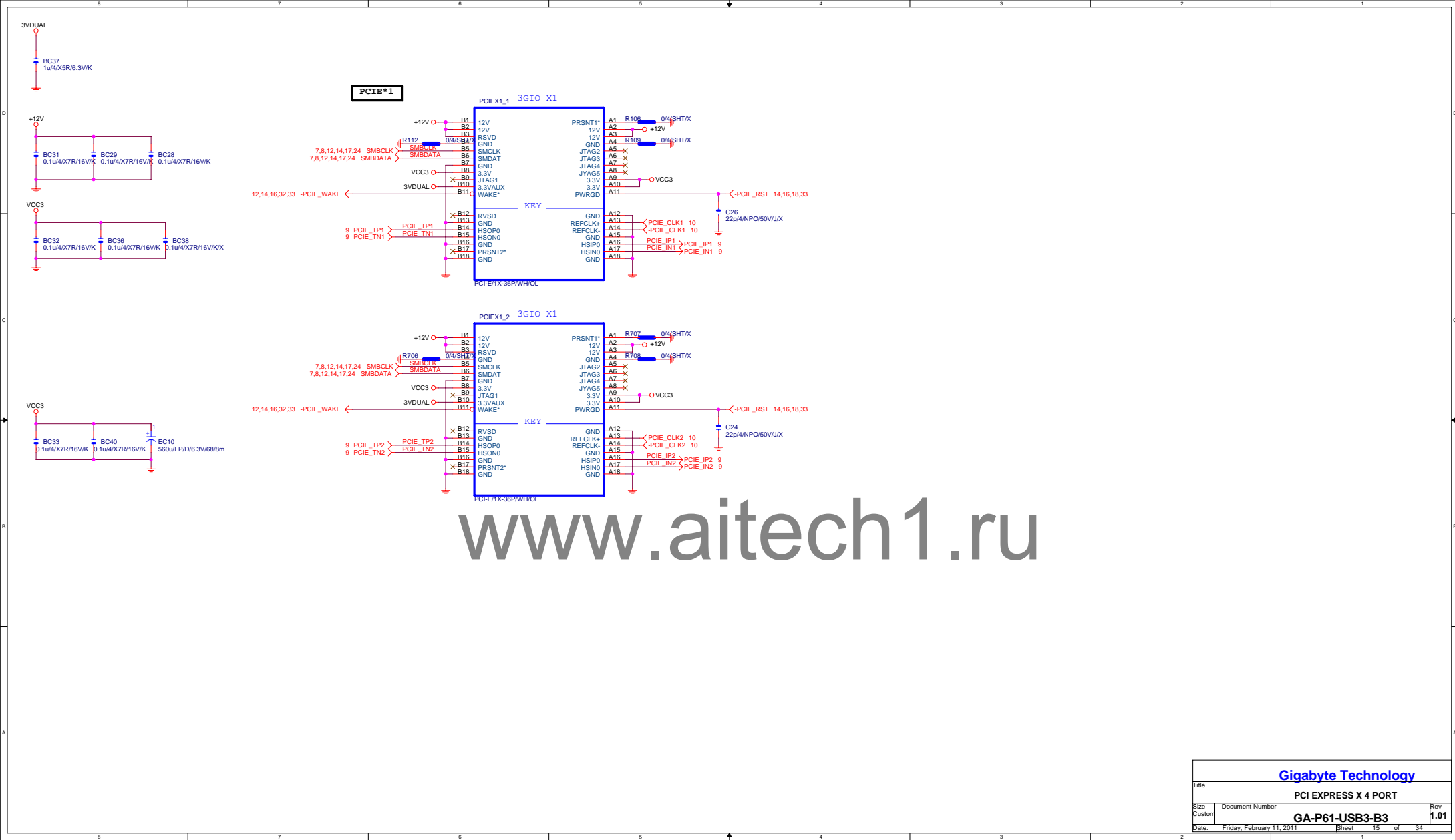








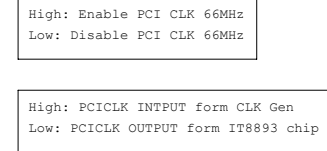


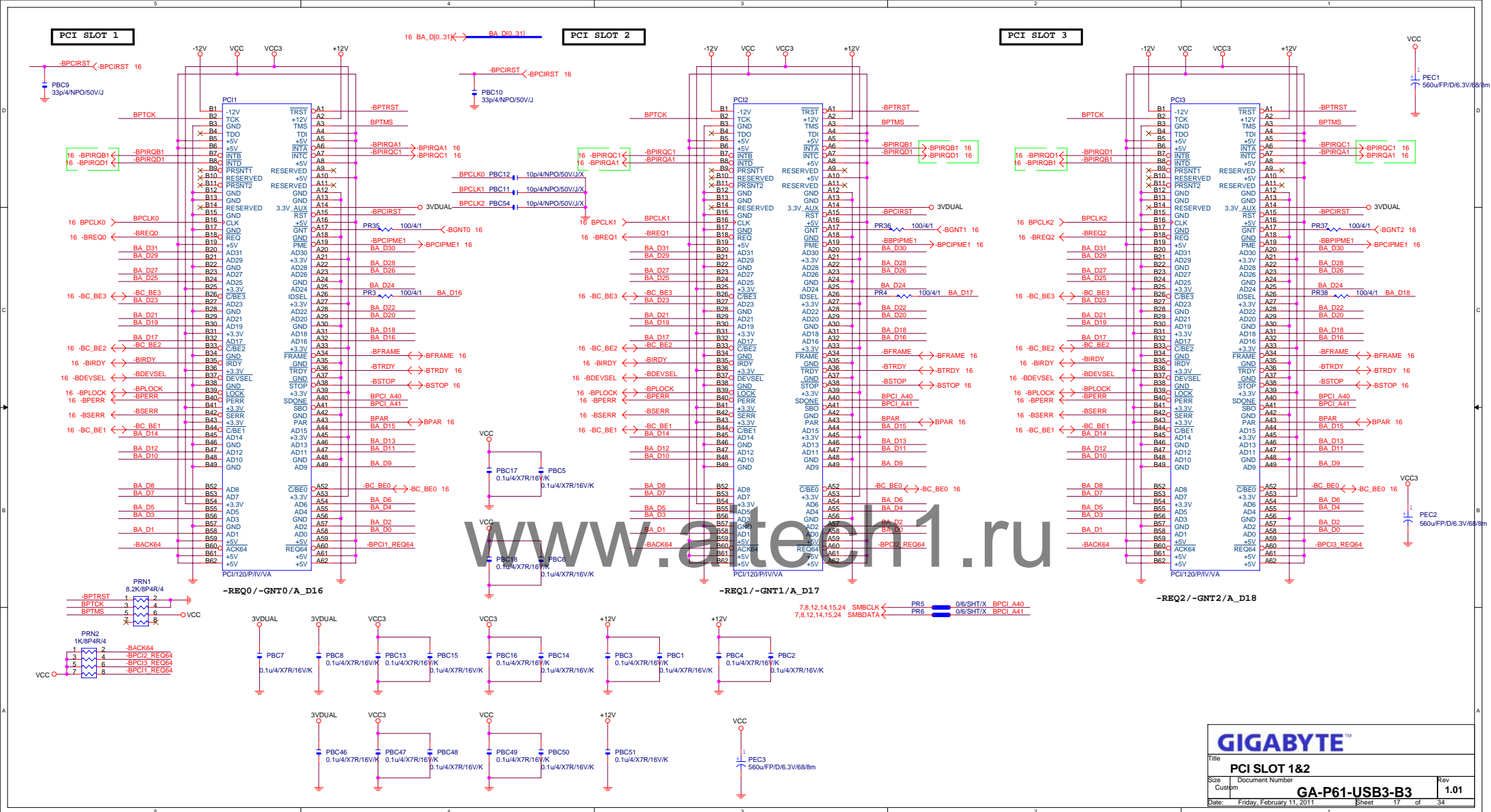


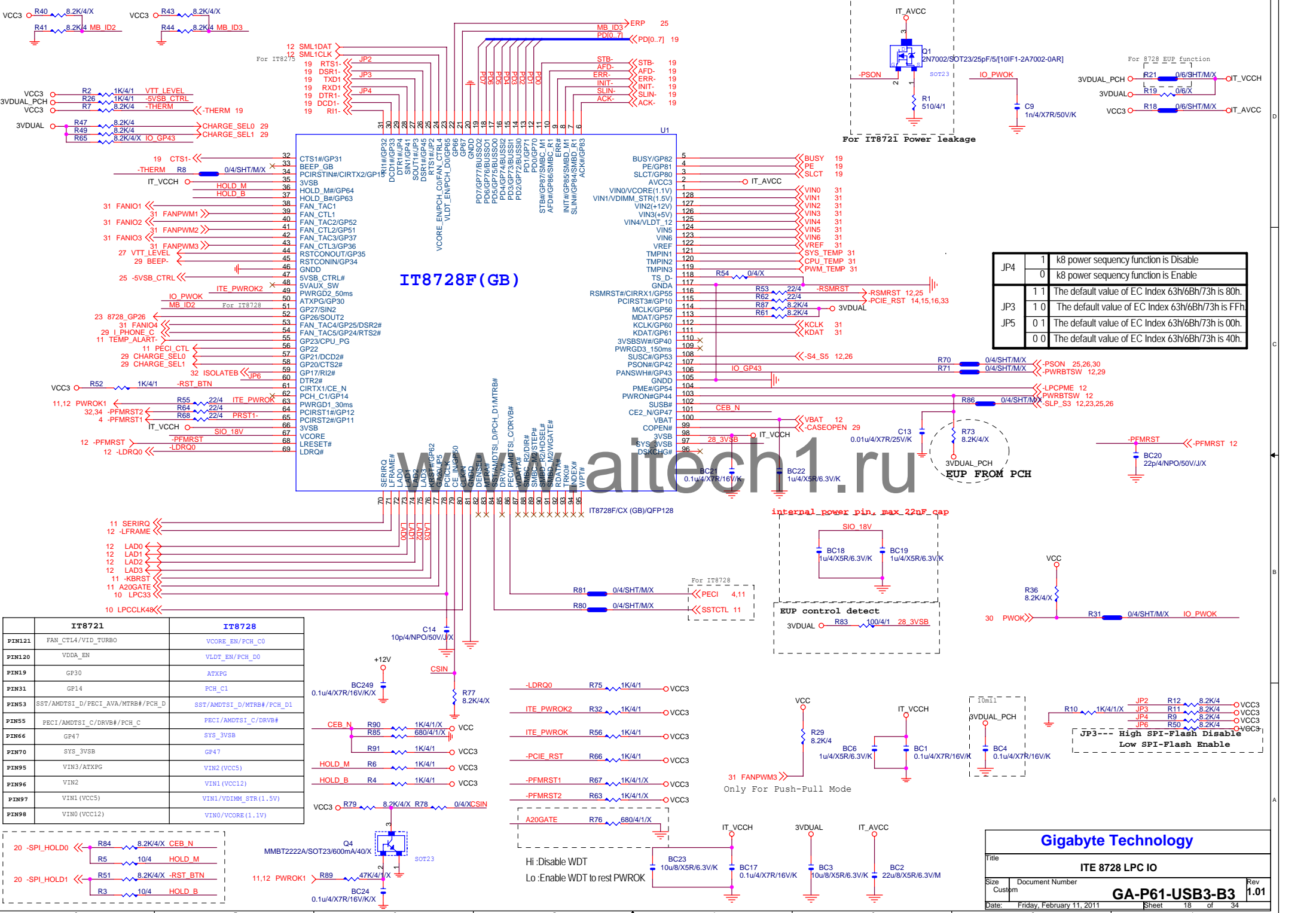
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Gigabyte Technology		
PCI EXPRESS X 4 PORT		
Size Custom	Document Number	Rev 1.01
GA-P61-USB3-B3		
Date: Friday, February 11, 2011	Sheet 15	of 34

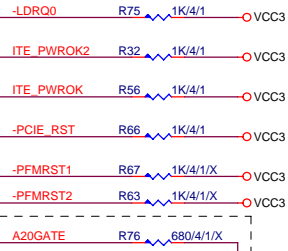
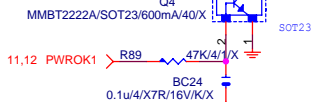
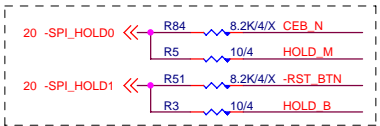






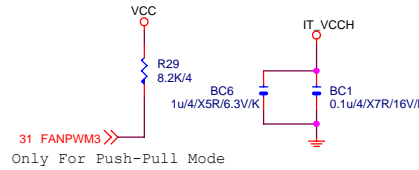


	IT8721	IT8728
PIN121	FAN_CTL4/VID_TURBO	VCORE_EN/PCH_C0
PIN120	VDDA_EN	VLDT_EN/PCH_D0
PIN19	GP30	ATXPG
PIN31	GP14	PCH_C1
PIN53	SST/AMDTSI_D/PECI_AVA/MTRB#/PCH_D	SST/AMDTSI_D/MTRB#/PCH_D1
PIN55	PECI/AMDTSI_C/DRVB#/PCH_C	PECI/AMDTSI_C/DRVB#
PIN66	GP47	SYS_3VSB
PIN70	SYS_3VSB	GP47
PIN95	VIN3/ATXPG	VIN2 (VCC5)
PIN96	VIN2	VIN1 (VCC12)
PIN97	VIN1 (VCC5)	VIN1/VDIMM_STR(1.5V)
PIN98	VIN0 (VCC12)	VIN0/VCORE(1.1V)



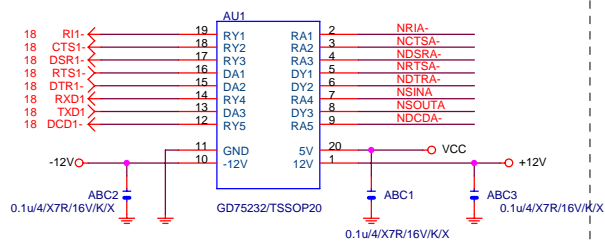
Hi :Disable WDT

Lo :Enable WDT to rest PWROK

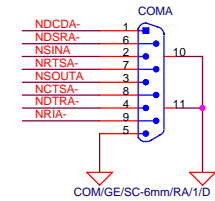
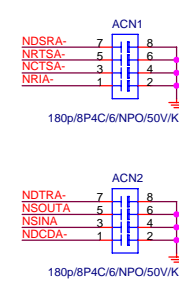
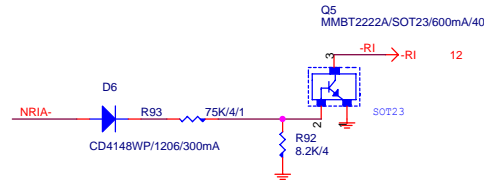


Gigabyte Technology			
Title			
ITE 8728 LPC IO			
Size	Document Number	Rev	
Custom		GA-P61-USB3-B3 1.01	
Date:	Friday, February 11, 2011	Sheet	18 of 34

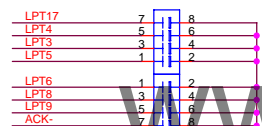
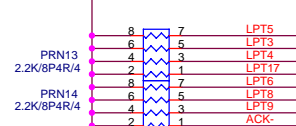
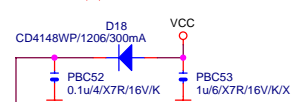
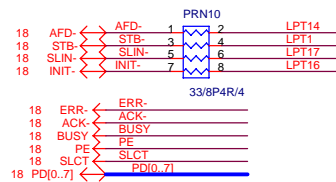
## COMA



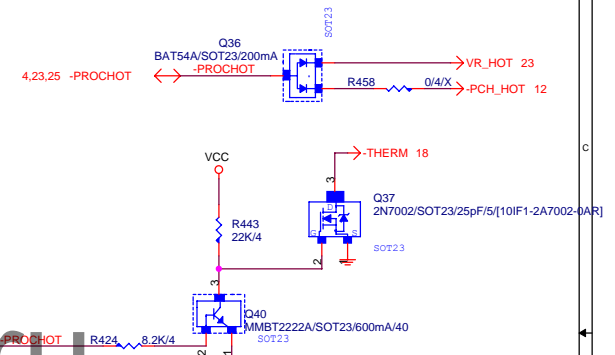
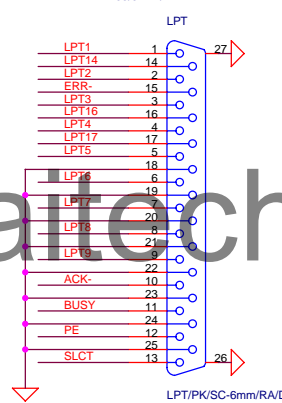
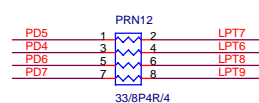
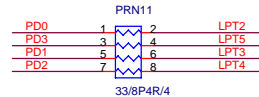
## COM RI



## LPT PORT

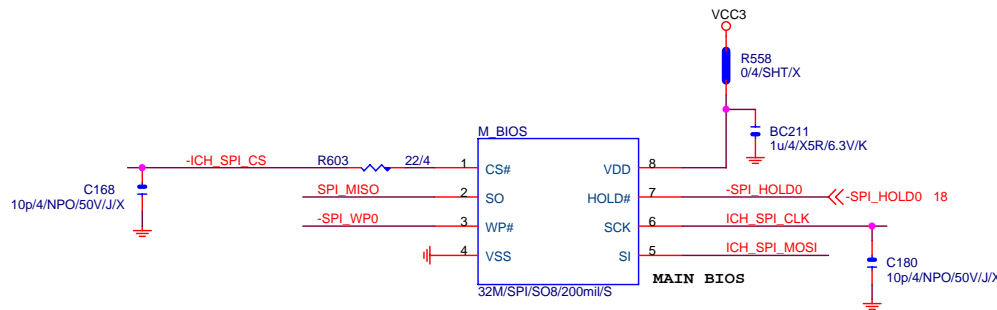
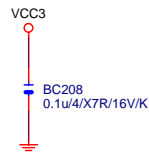


PC1 180p/4/NPO/50V/J

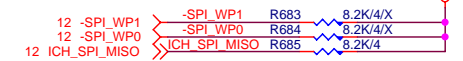
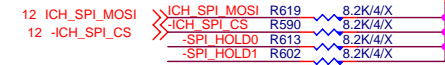


## Gigabyte Technology

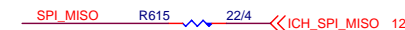
Title			
COM & PROHOT/Dynamic O.C.			
Size	Document Number	Rev	
Custom		GA-P61-USB3-B3 1.01	
Date:	Friday, February 11, 2011	Sheet	19 of 34



### MOSI For DMI RX Termination Voltage



### Default int pull up



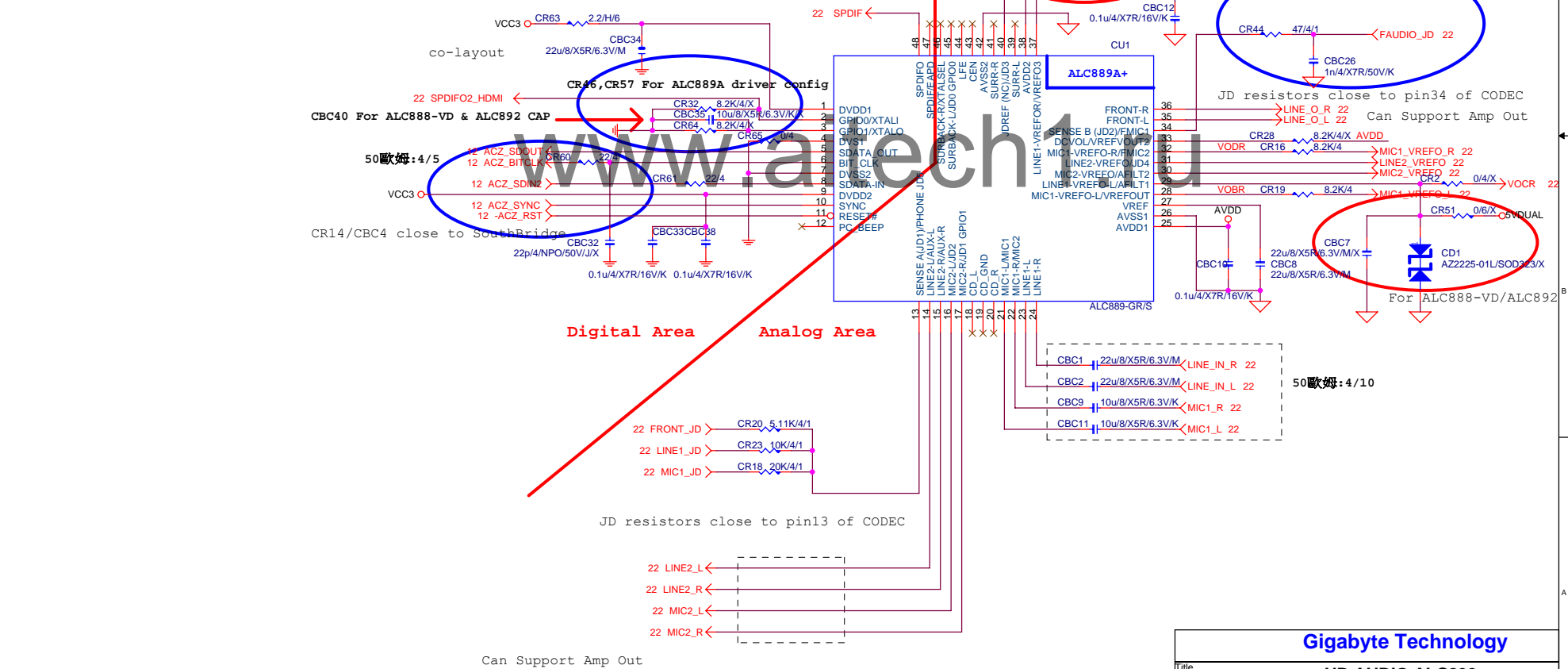
BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

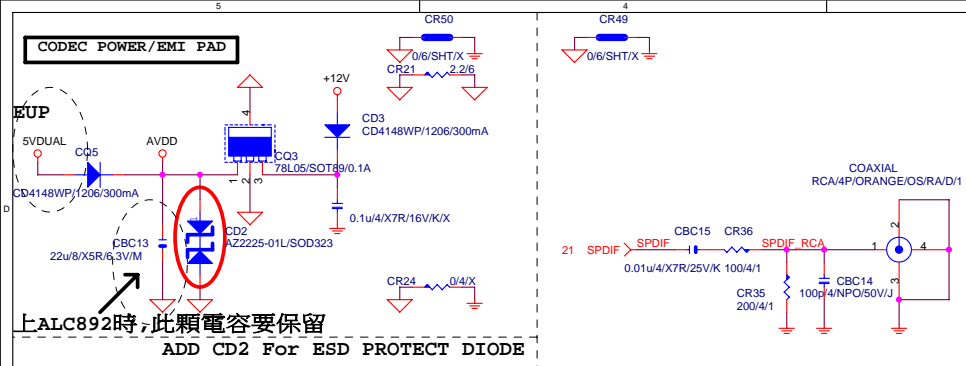
1 means floating  
0 means PD 1K

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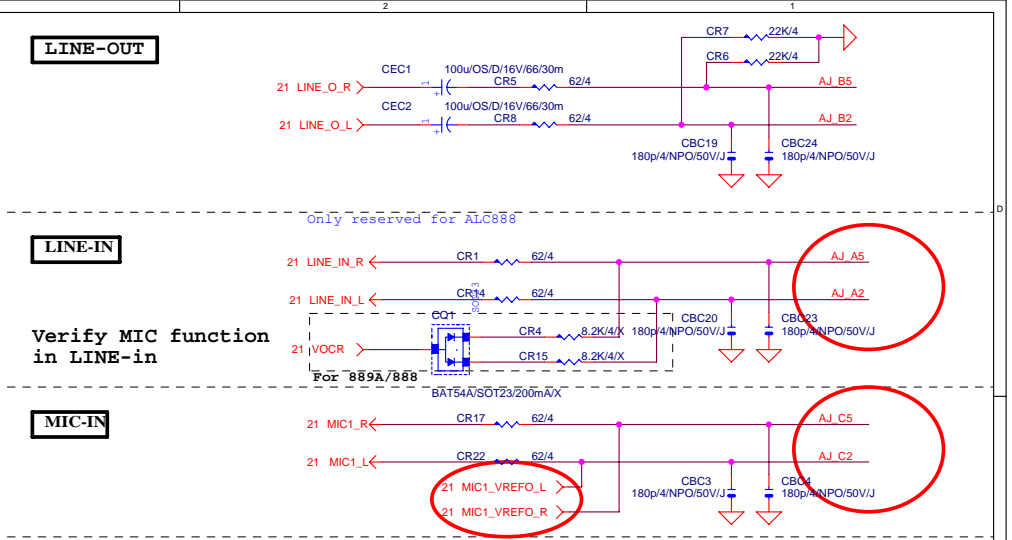
Gigabyte Technology			
Title		BIOS	
Size	Document Number	GA-P61-USB3-B3	
Custom			Rev 1.01
Date:	Friday, February 11, 2011	Sheet 20	of 34

	ALC883	ALC888-VA	ALC888B	ALC888-VD	ALC892R	ALC889	ALC889A
CR32	X	X	X	X	X	X	O
CR64	X	X	X	X	X	X	O
CR65	O	O	X	X	X	O	O
CBC35	X	X	X	10uF/X5R10uF/X5R	X	X	X
CR28	O	X	X	X	X	X	X
CR34	20K/1%	20K/1%	20K/1%	20K/1%	20K/1%	20K/1%	20K/0.1%
CR31	X	X	O	X	O	O	X
CR30	O	O	X	O	X	X	O
CBC1/CBC2/CBC5/ CBC6/CBC9/CBC11	4.7uF /X5R	4.7uF /X5R	4.7uF /X5R	4.7uF /X5R	4.7uF /X5R	10uF /X5R	4.7uF /X5R
CR5/CR8/CR11/CR4/ CR17/CR22/CR45/CR33/ CR47/CR40/CR26/CR37/ CR13/CR11/CR57/CR53	75 ohm	75 ohm	75 ohm	75 ohm	75 ohm	66 ohm or lower	75 ohm
CR51/CD1/CBC7	X	X	X	O	X	X	X
CD2/CD3/CQ3/CQ5	O	O	O	X	O	O	O

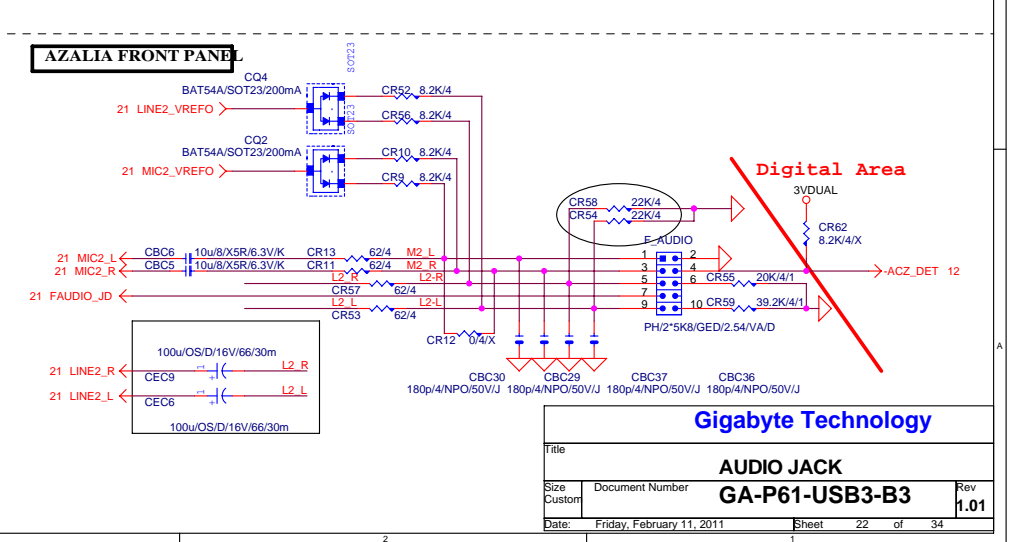
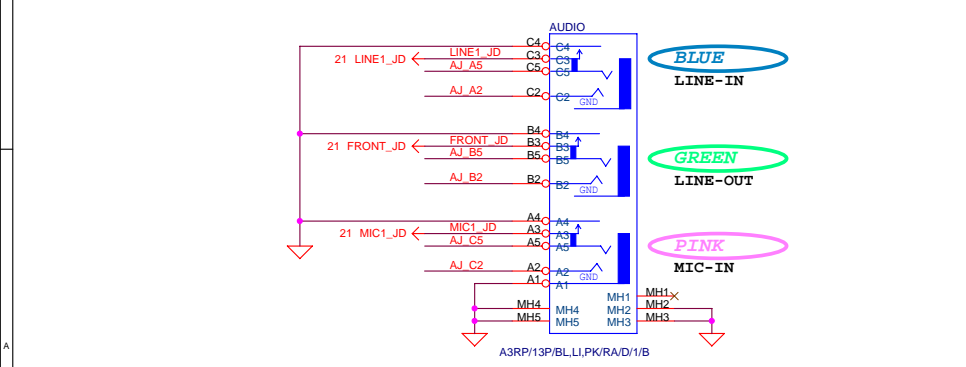
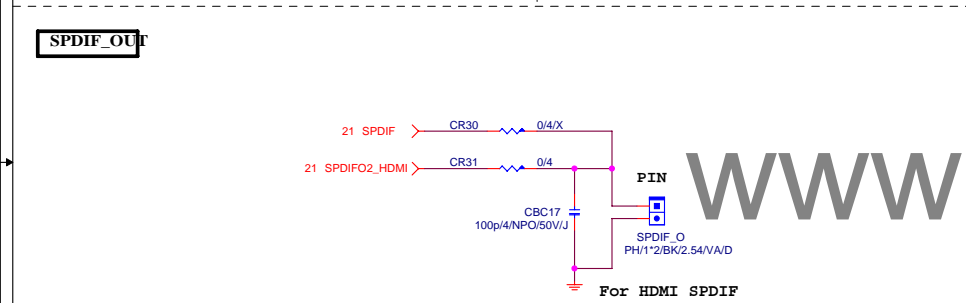




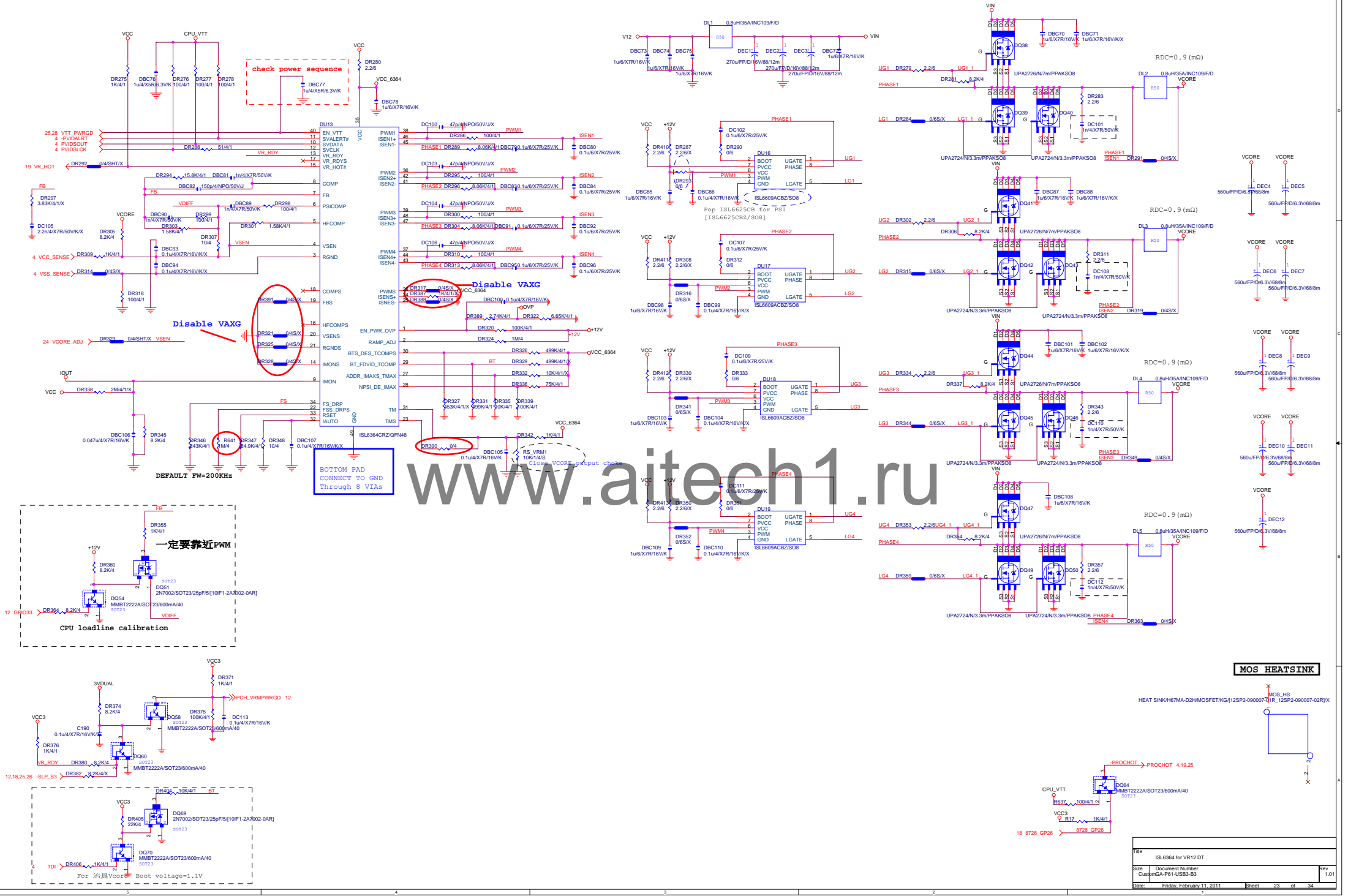
上ALC892時,此顆電容要保留  
ADD CD2 For ESD PROTECT DIODE



Verify MIC function in LINE-in







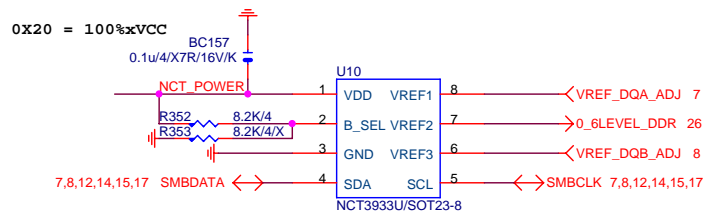
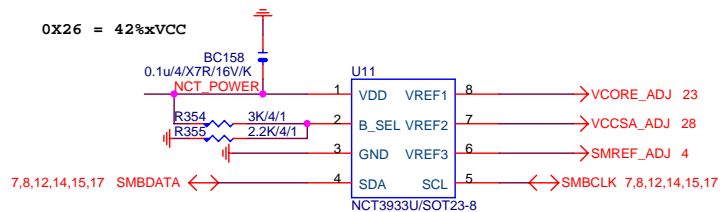
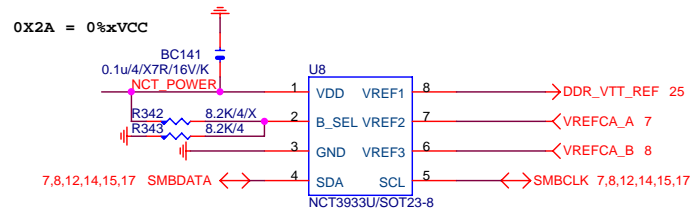
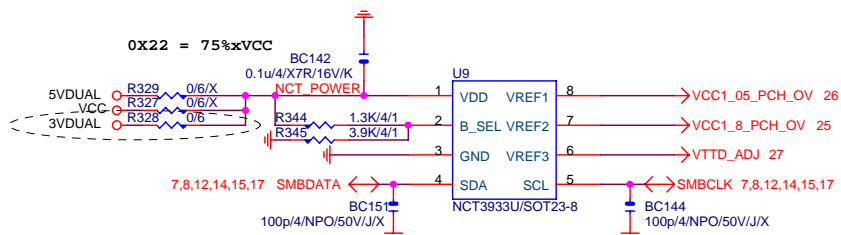
MOS HEATSINK

HEAT SINK:H67MA-D2H1MOSFET.KG(12SP2-090007R\_12SP2-090007-02RJ)X

PROCHOT

PROCHOT 4.19,25

File	ISL6364 for VR12 DT	Rev	1.01
Size	Document Number		
Customer	Customer Name		
Date	Friday, February 11, 2011	Sheet	23 of 34

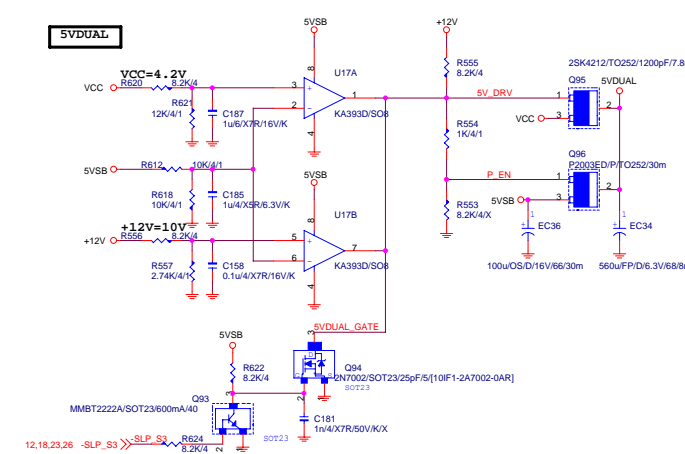


up6262	0X2A	0X20	0X22	0X26
VREF1	DDRVTT	VREF_DDRA_DQ	VCC1_05_PCH	VCORE
VREF2	VREF_DDRA_CA	DDR15V	VCC1_8_PCH	VCCSA
VREF3	VREF_DDRA_CAVREF_DDRB_DQ	CPU_VTT	SMREF	

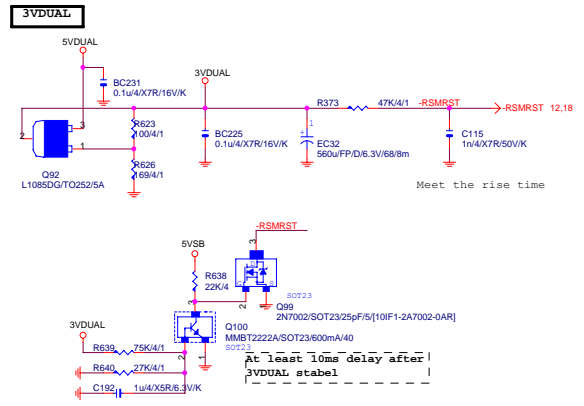
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Gigabyte Technology			
Title			
CPU CORE VR-2			
Size	Document Number	Rev	
Custom		GA-P61-USB3-B3 1.01	
Date:	Friday, February 11, 2011	Sheet	24 of 34

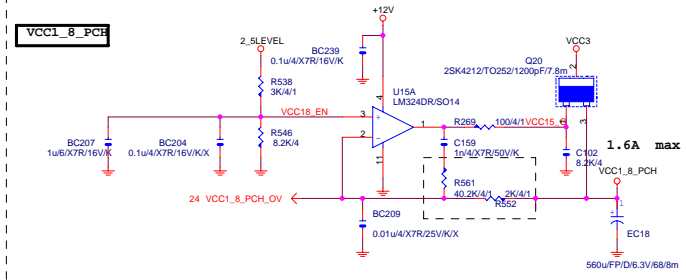
# 5VDUAL



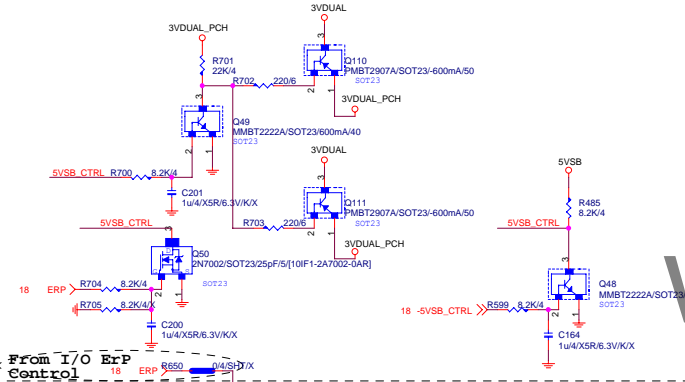
# 3VDUAL



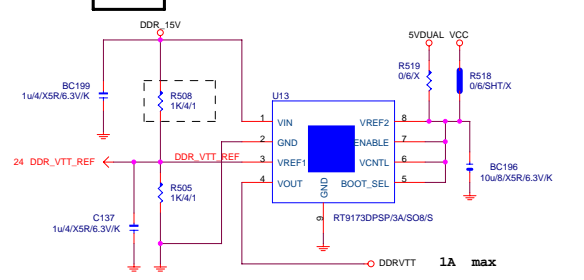
# VCC1\_8\_PCH



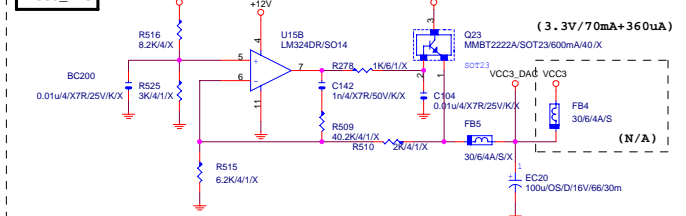
# I/O ErP Control



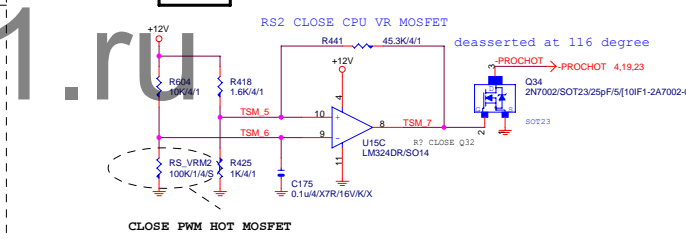
# DDRVTT



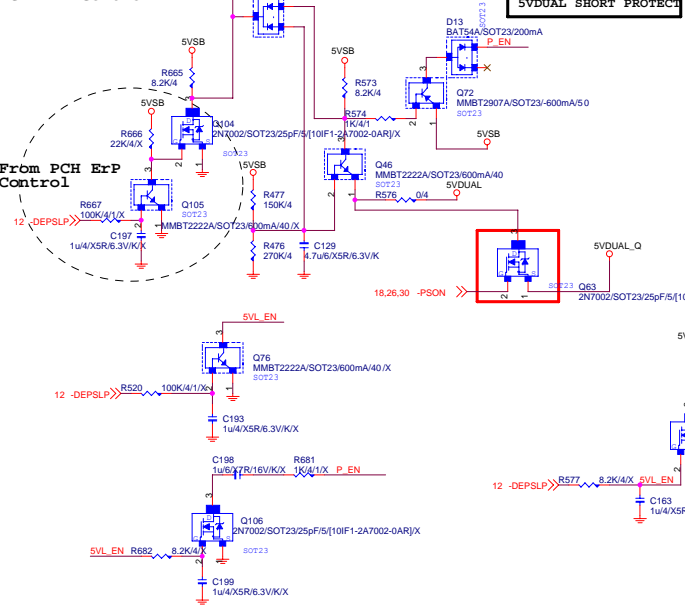
# VCC3\_DAC



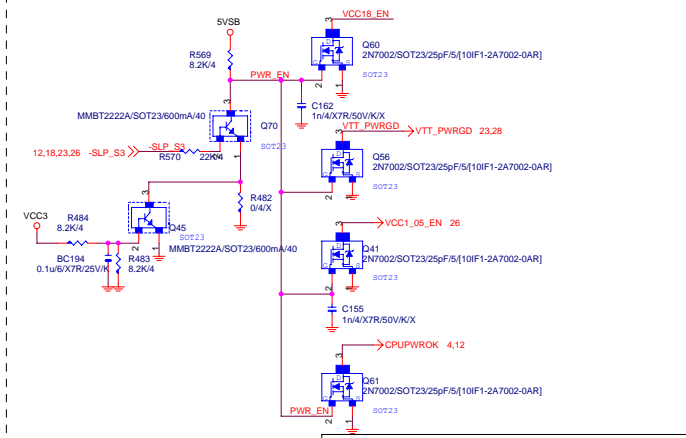
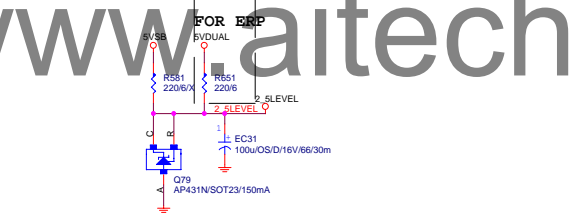
# -PROHOT



# PCH ErP Control



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Gigabyte Technology			
DISCRETE POWER			
Size	Document Number	GA-P61-USB3-B3	
C		Rev 1.01	
Date	Friday, February 11, 2011	Sheet	26 of 34

VIN=5V,VOUT=1.5V,IOUT=25A,PHASE=1  
IRMS=11.45A  
560u/FP/D/6.3V/68/8m RIPPLE CURRENT=4.7A  
Coefficient=1.7(85°C),1(105°C)  
VIN Ripple current=4.7X1.7=7.99A(85°C)  
-->故固態電容須2X7.99=15.98>11.45A

Remote sense請從最重的負載端點拉回

$$0.6 * [(R_S + R_O) / R_O] = V_{out} = 1.54V$$

**POWER ISSUE**

The schematic diagram illustrates the power section of the PCB, showing the connection of the power supply to the microcontroller and the DDR2 memory. The diagram includes the power supply input (12, 18, 25, 30V), the power switch (Q65), the power MOSFET (Q51), the power diode (R496), the power capacitor (C131), the DDR2 memory (Q64), the DDR2 memory capacitor (R571), the DDR2 memory diode (R572), and the DDR2 memory MOSFET (Q71). The diagram is labeled "POWER ISSUE".

注意：Rocset的阻值要依據Lo side Rdson改變  
一般Ipeak設定在50~60A即可

POWER ISSUE 1u/6/X7R/16V/K

$V_{IN}=3.3V$ ,  $V_{OUT}=1.05V$ ,  $I_{OUT}=7.5A$ ,  $PHASE=1$   
 $IRMS=3.4875A$   
 $1000u/D/6.3V/8C/30m$  RIPPLE CURRENT=1.14A  
 Coefficient=1.7(85°C), 1(105°C)  
 $V_{IN}$  Ripple current=1.14X1.7=1.938A(85°C)  
 -->故電解電容須 $2 \times 1.938 = 3.876 > 3.4875A$

Remote sense請從最重的負載端點拉回

$$0.6 * [(R_S + R_O) / R_O] = V_{out} = 1.07V$$

## CPU\_VTT

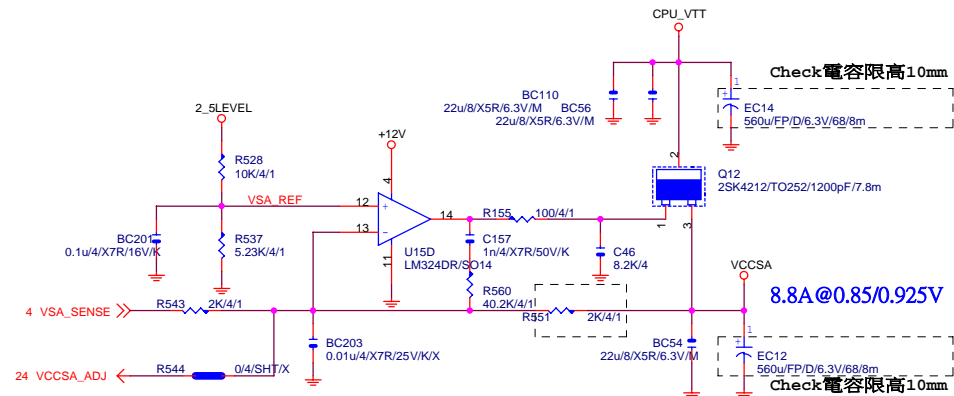
VTT_SEL	
HI	1.05V
LO	1.0V

BOTTOM PAD  
CONNECT TO GND  
THROUGH 4 VIA

F_SEL	
FU	1MHz
PD	600KHz
NC	500KHz
BIOS gnd	300KHz

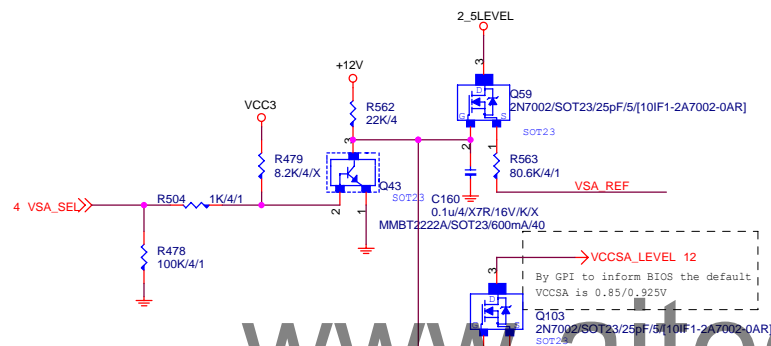
GIGABYTE™		
Title		
CPU_VTT_PWM_ISL6322CRZ		
Size	Document Number	Rev
Custm	GA-P61-USB3-B3	1.01
Date:	Friday, February 11, 2011	Sheet 27 of 34

# VCC\_SA

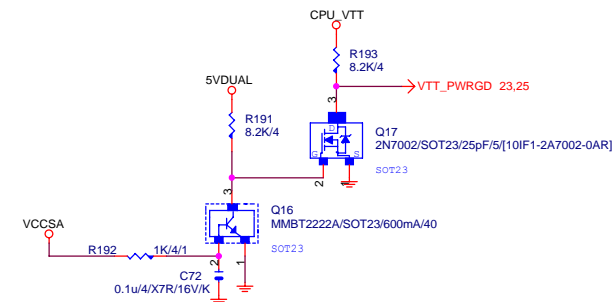
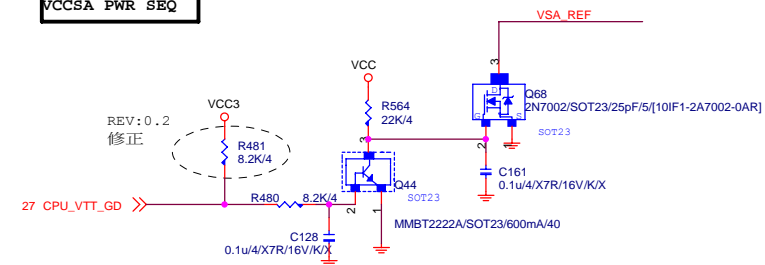


PDG 1.01

	VSA_SEL
HI	0.85V
LO	0.925V



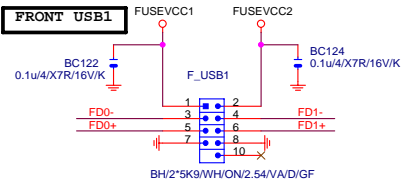
# VCCSA PWR SEQ



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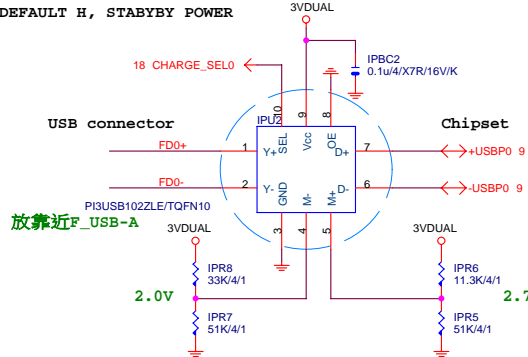
# Gigabyte Technology

Title		
CPU VTT PWM_ISL6312		
Size	Document Number	Rev
Custom	GA-P61-USB3-B3	1.01
Date:	Friday, February 11, 2011	Sheet 28 of 34

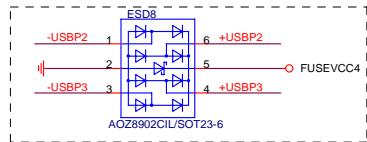
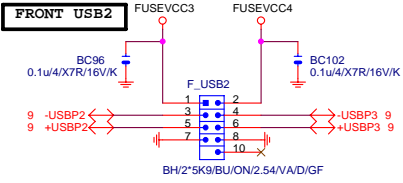
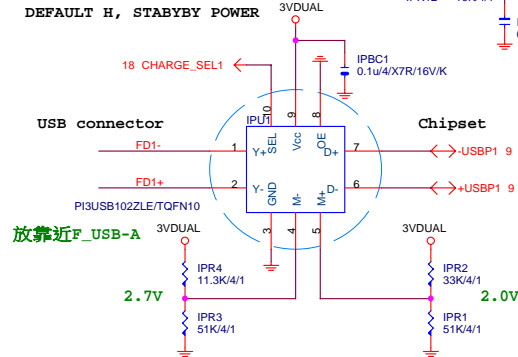


### i\_phone charger circuit

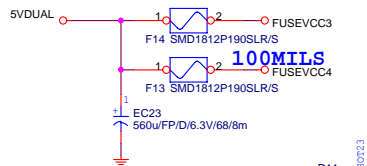
DEFAULT H, STABBY POWER



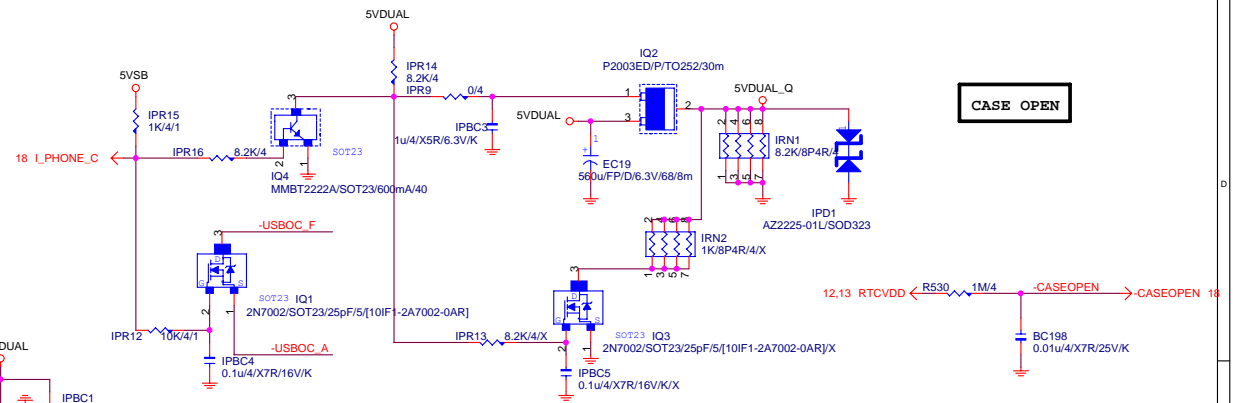
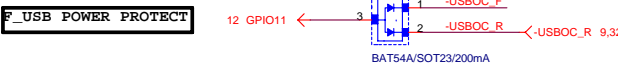
DEFAULT H, STABBY POWER



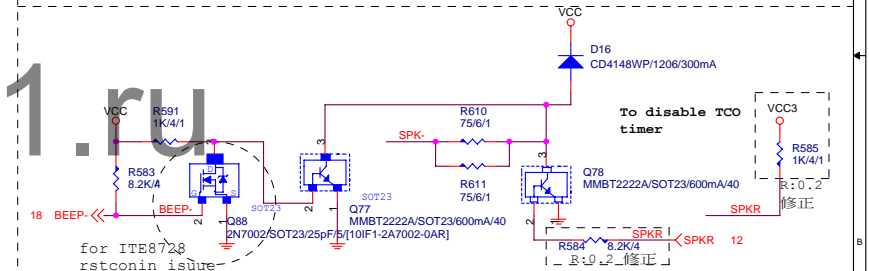
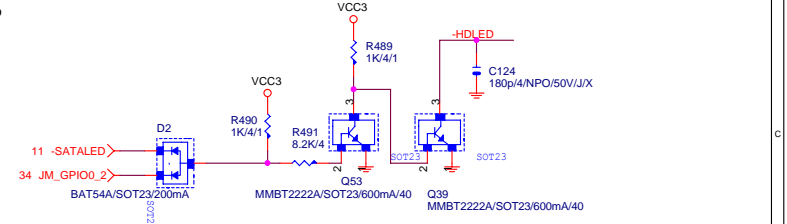
Close to connector



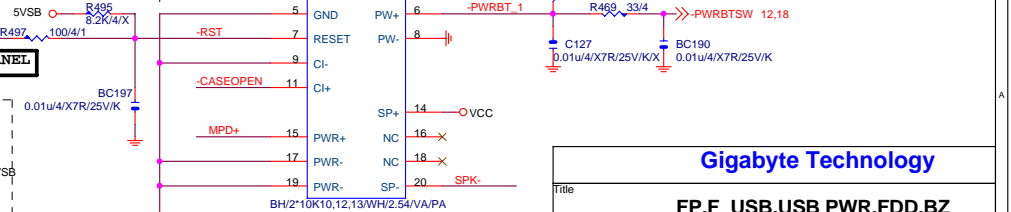
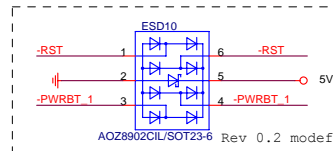
### F\_USB POWER PROTECT



### SATA LED



### INTEL FRONT PANEL



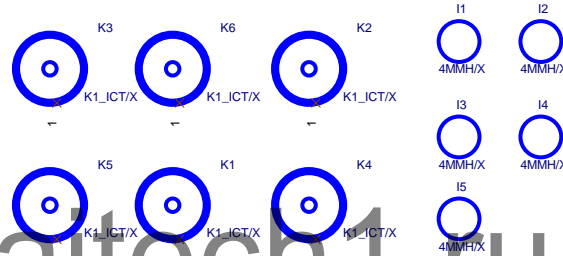
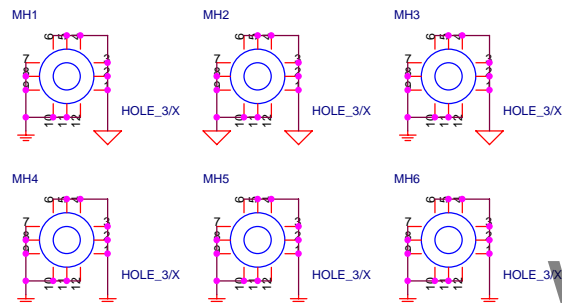
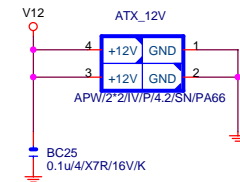
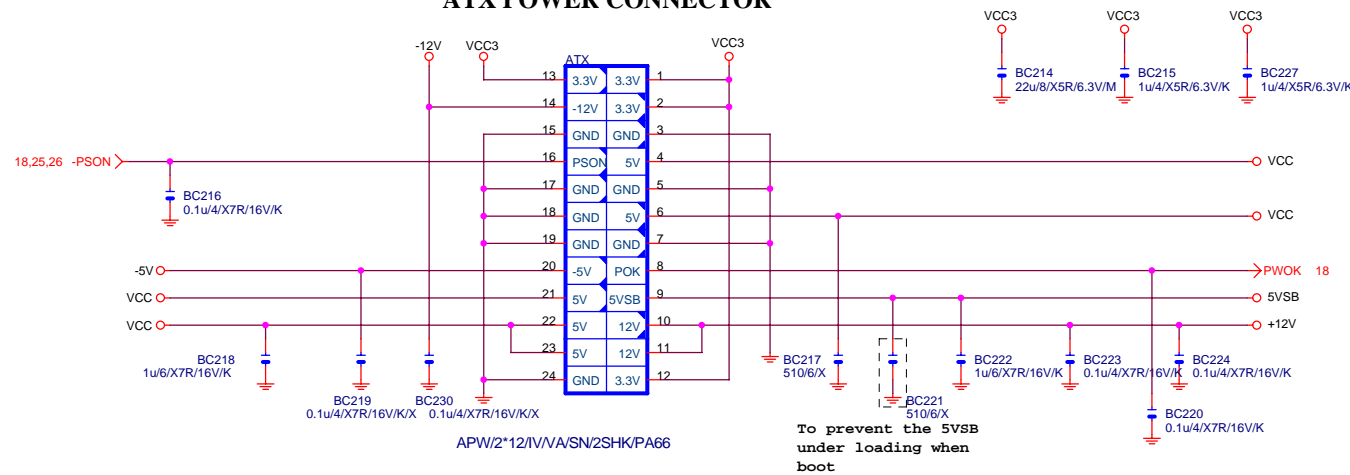
Gigabyte Technology

Title	FF, F_USB, USB PWR, FDD, BZ		
Size	Custom	Document Number	GA-P61-USB3-B3
Date:	Friday, February 11, 2011	Sheet	29 of 34

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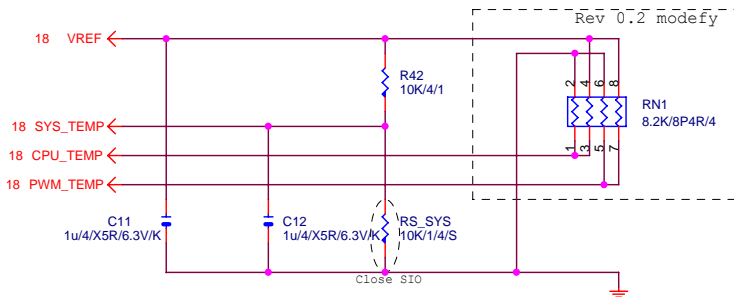
# ATX POWER CONNECTOR



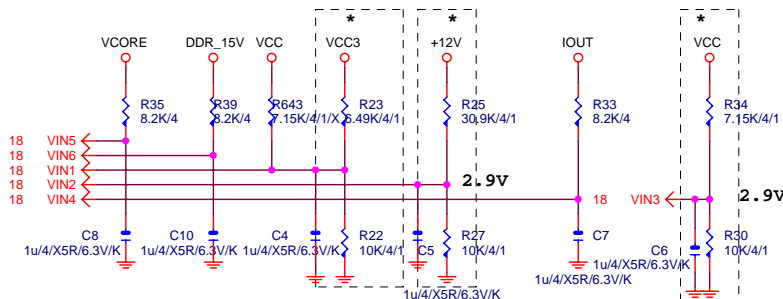
www.altech1.ru

Gigabyte Technology			
Title			
ATX POWER CONNECTOR			
Size	Document Number	Rev	
Custom	GA-P61-USB3-B3	1.01	
Date:	Friday, February 11, 2011	Sheet	30 of 34
	2		1

TEMP H/W MONITOR

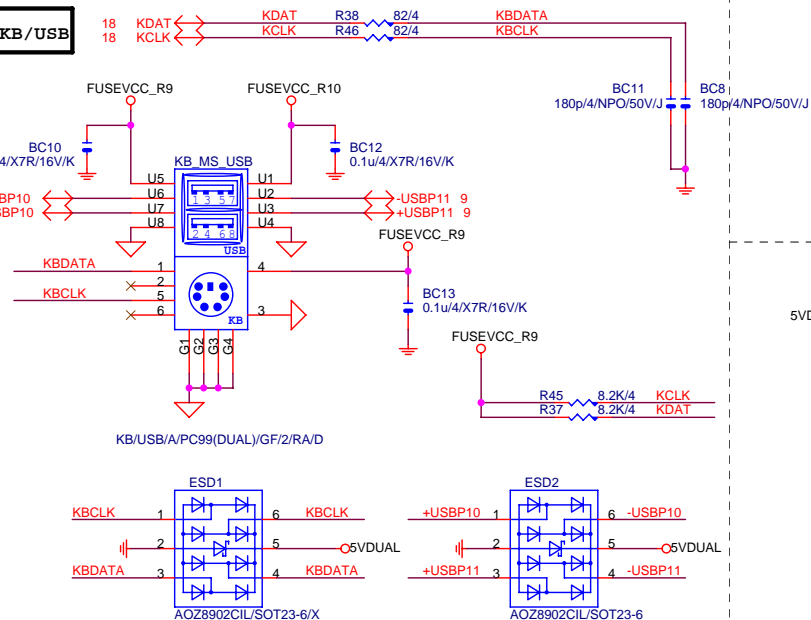


VOLTAGE-- H/W MONITOR

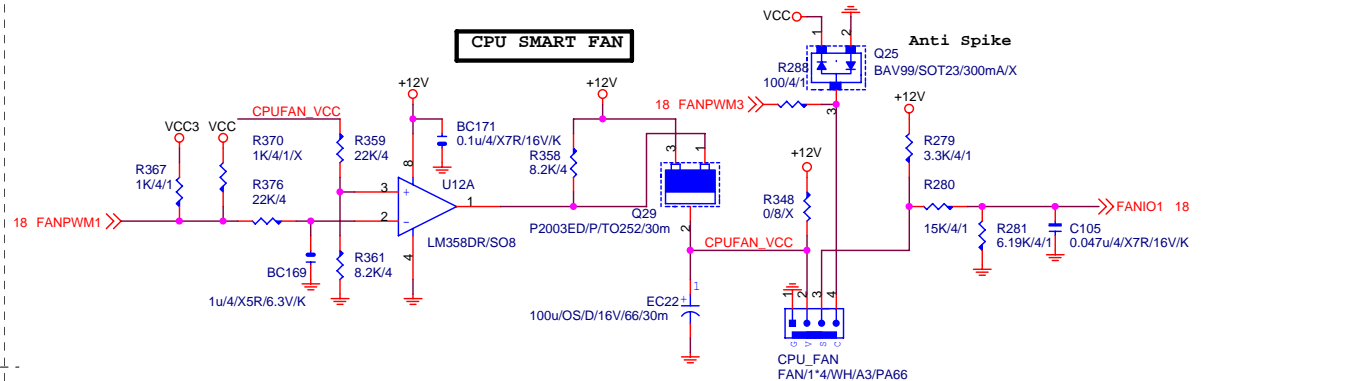


The division voltage of VIN2 & VIN3 must be around 2.9V

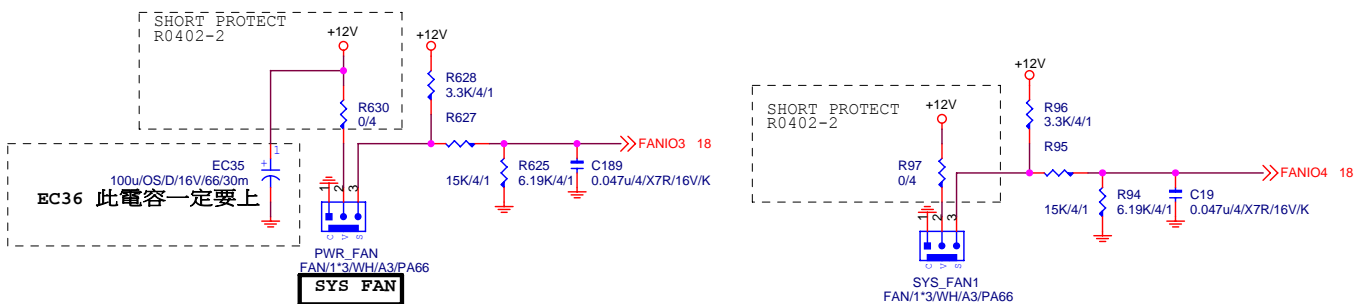
## KB/USB



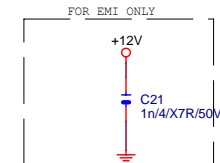
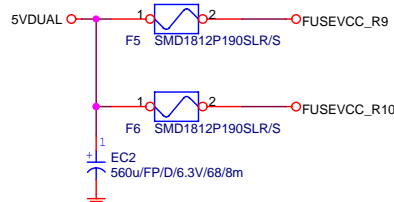
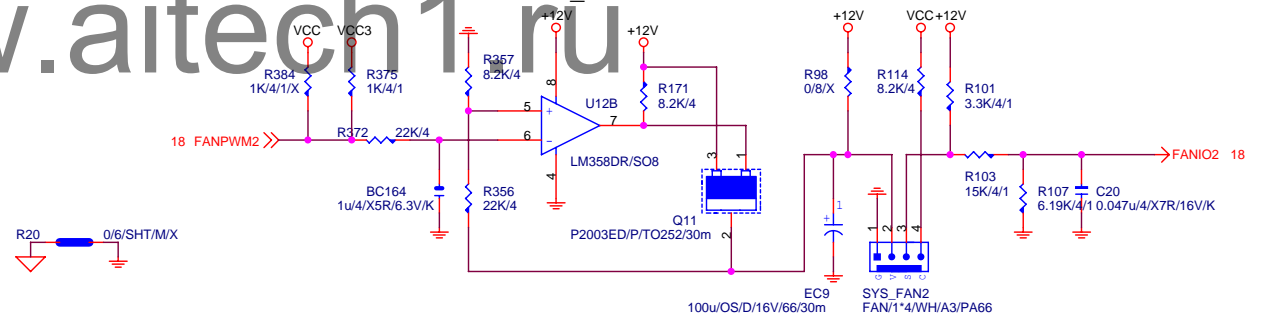
## CPU SMART FAN



SYS FAN



Linear SYS\_FAN

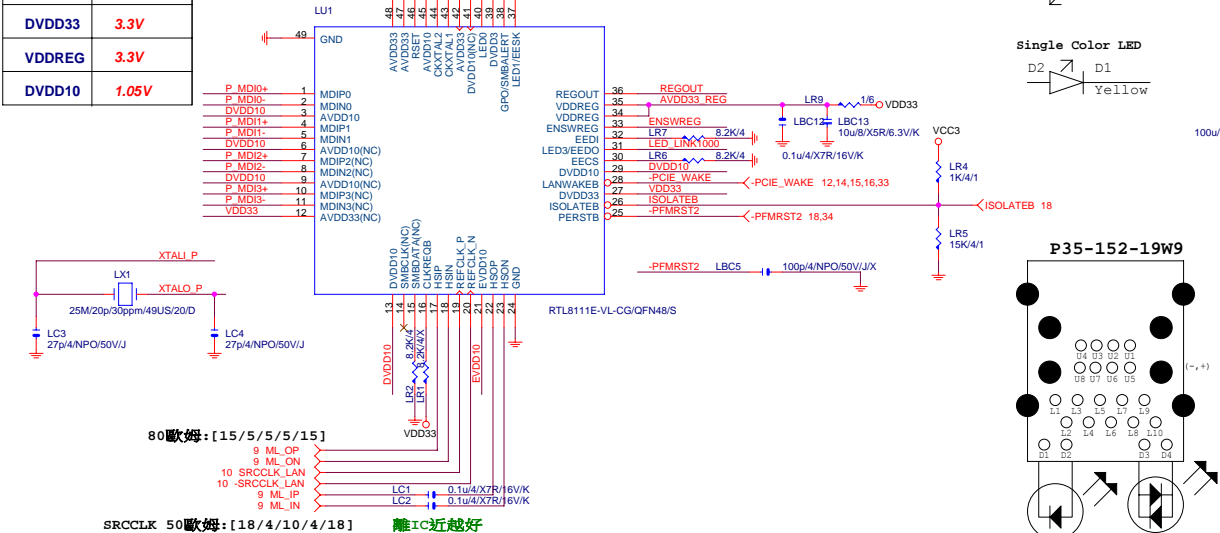


## Gigabyte Technology

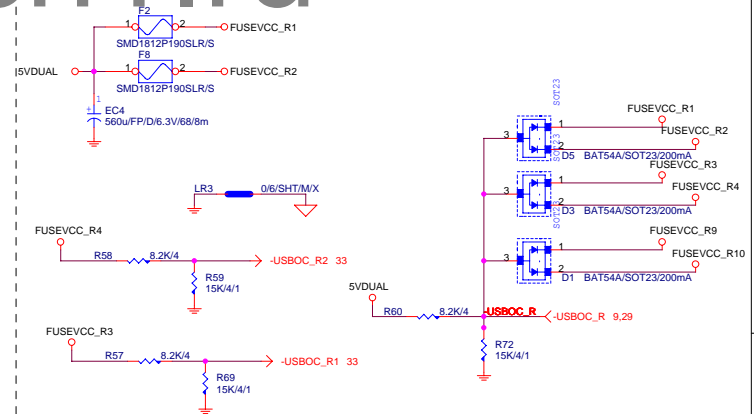
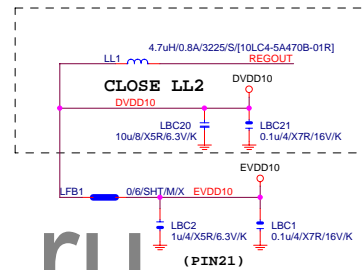
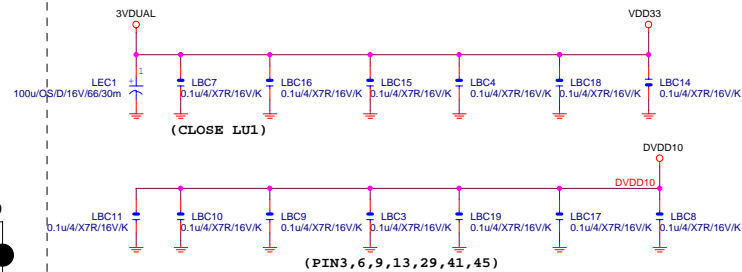
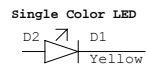
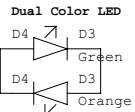
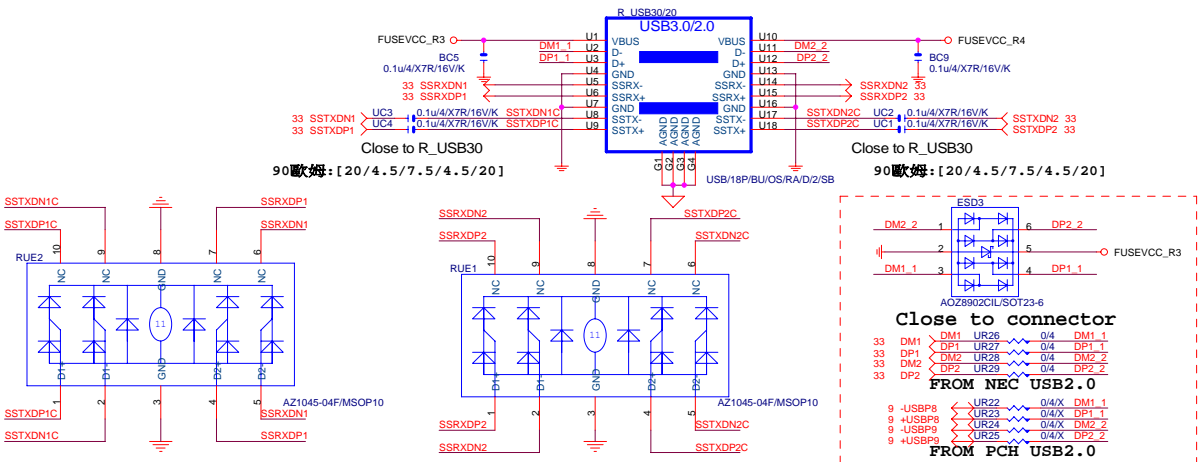
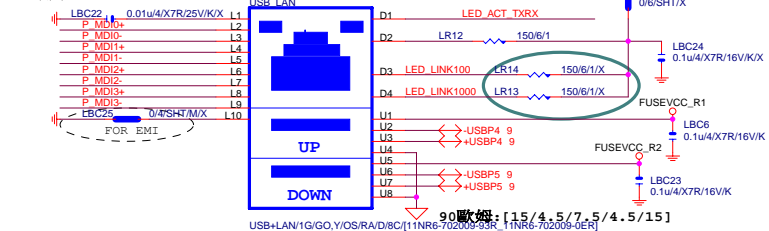
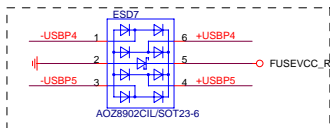
Title			
HWM,KB/MS, FAN CTRL			
Size	Document Number		Rev
Custom	GA-P61-USB3-B3		1.01
Date:	Friday, February 11, 2011	Sheet	31 of 34

### Power domain chart

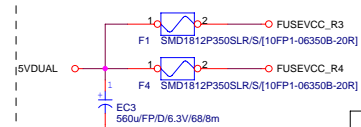
	RTL8111E
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V

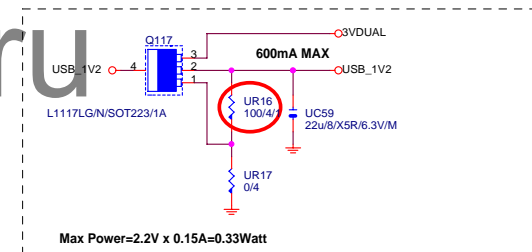
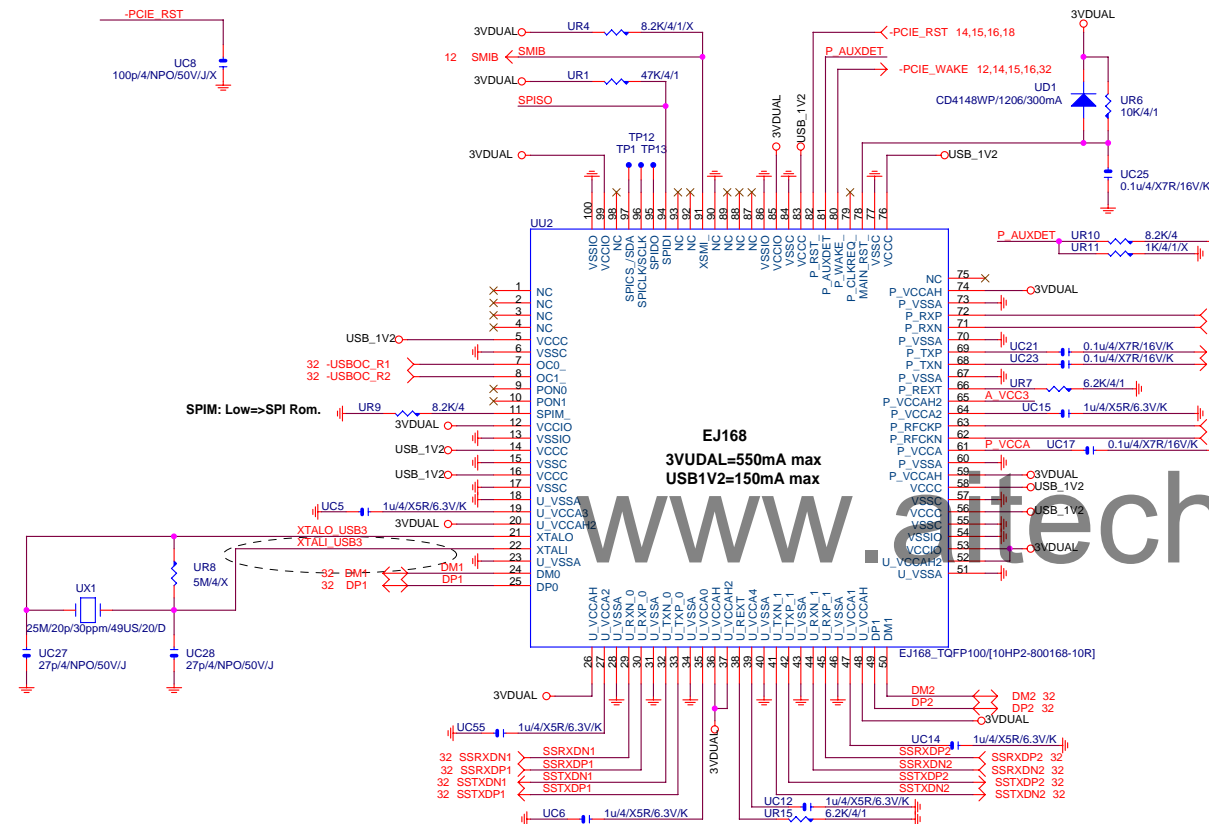
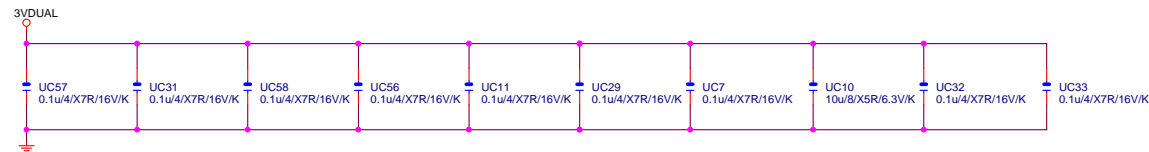


100歐姆:[20/4/8/4/20]



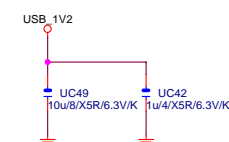
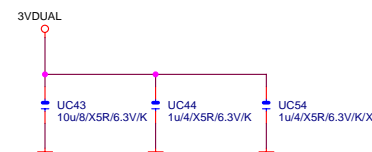
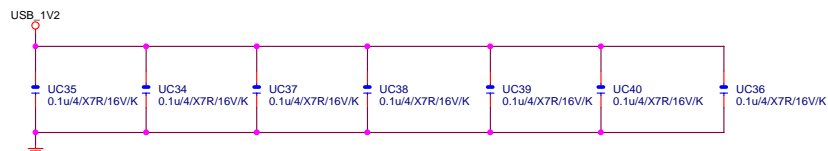
Close to connector





AZ1117H-1.2TR/SOT223/1A-->UR17:0/4,UR16:N/A [1.2V]

L1117LG/N/SOT223/1A-->UR17:0/4 ,UR16:100/4/1 [1.25V]



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Title		EJ168
Size	Document Number	GA-P61-USB3-B3
Custom	Rev	1.01
Date:	Friday, February 11, 2011	Sheet 33 of 34

請注意：  
此頁皆為MASK的零件  
Footprint不同

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